

# Automated Industrial Wind Tunnel Network Control with LabVIEW

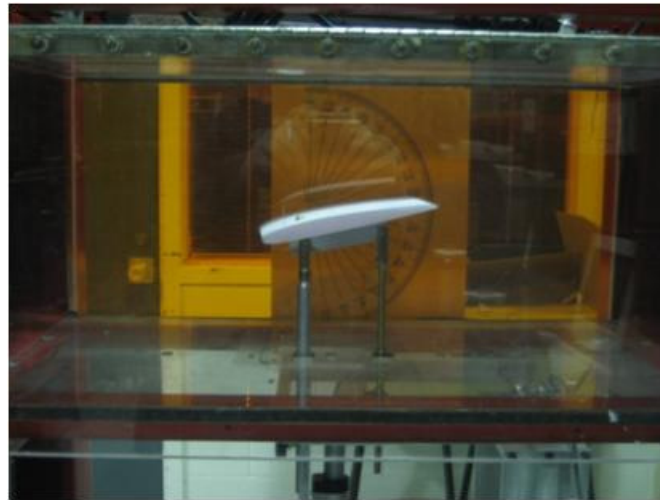
Matt Draear

Advisor: Dr. Malinowski

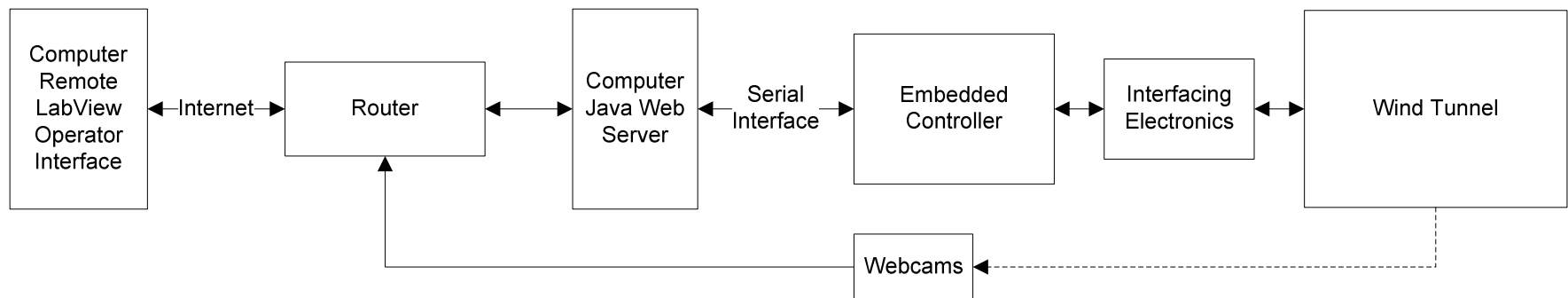
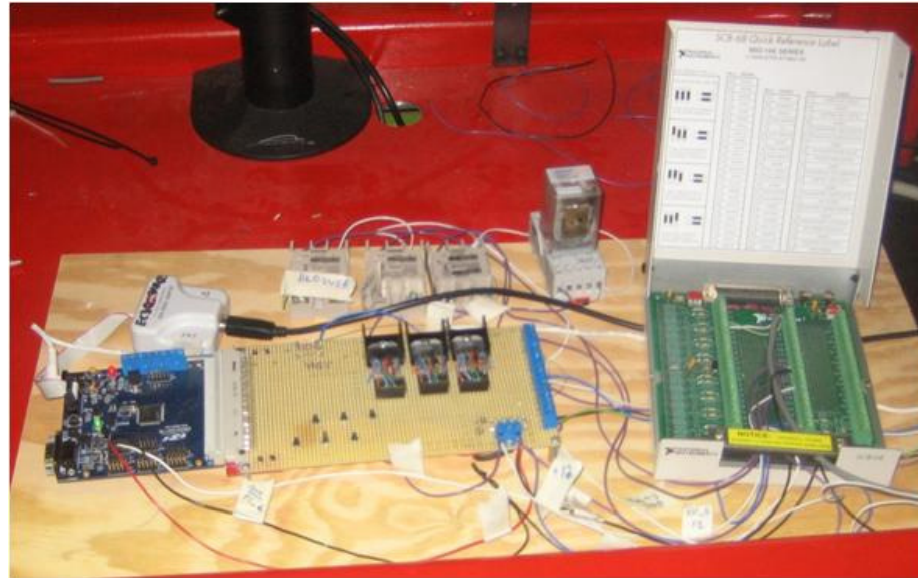


# Presentation Outline

- Overview of Old Hardware
- Overview of New Hardware
- Details of New Hardware
- FPGA LabVIEW Code
- Real-Time Controller LabVIEW Code
- Wind Tunnel Test Results



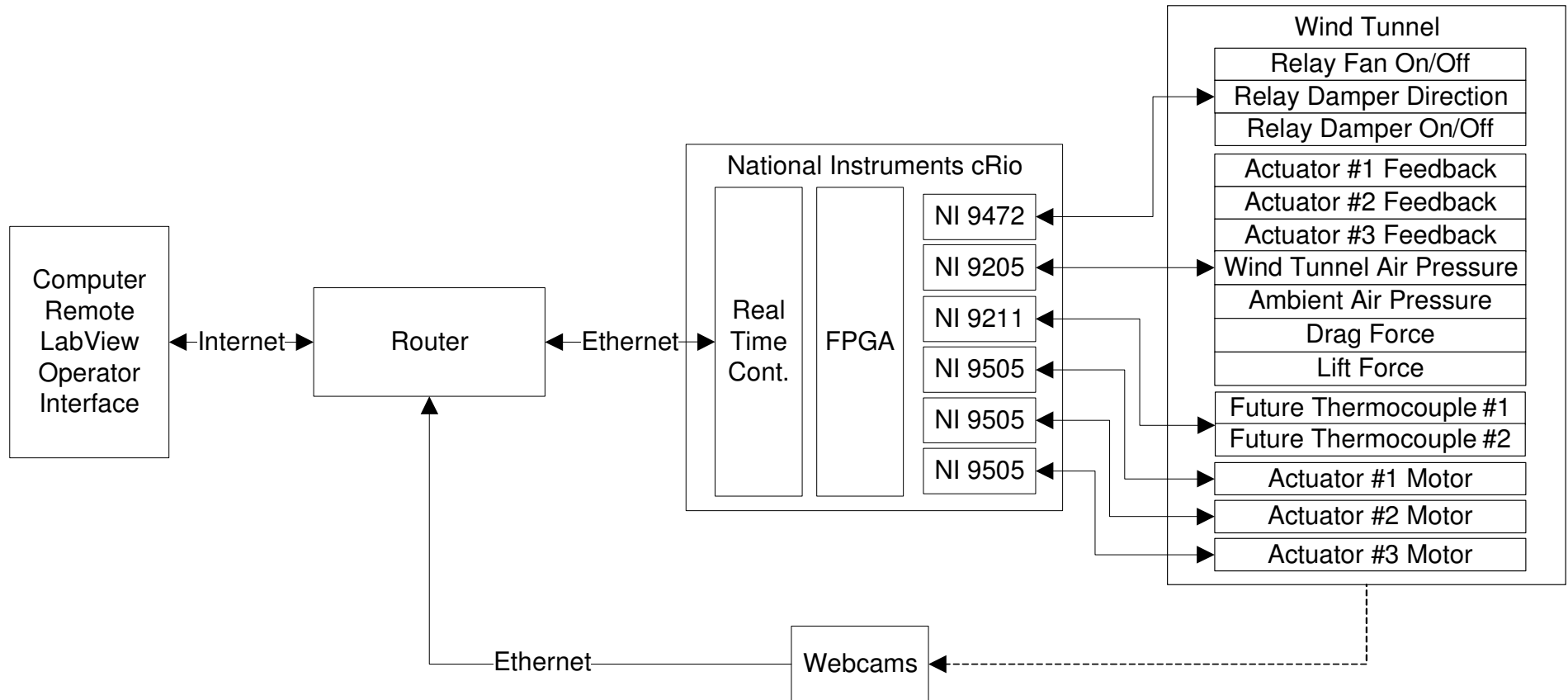
# Old Hardware Overview



# New National Instruments Hardware



# System Diagram With New Hardware



# National Instruments cRIO – 9074



# Analog Input NI - 9205



# Thermocouple Input NI - 9211

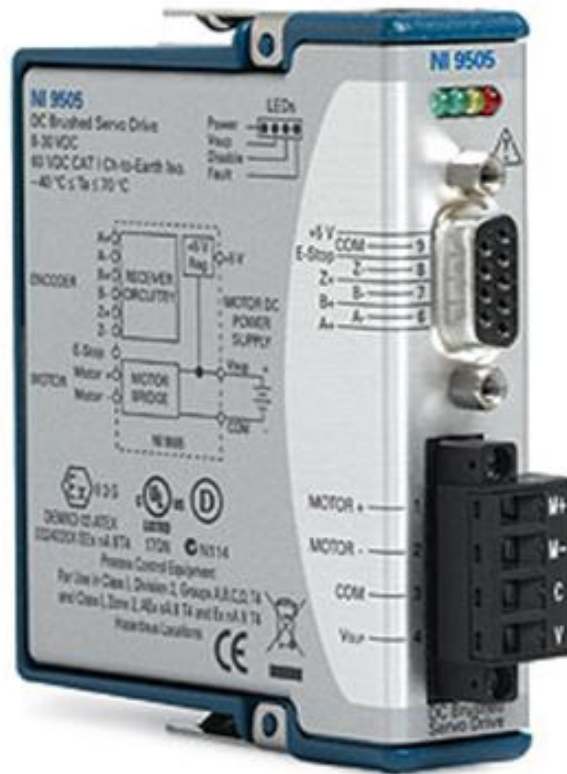




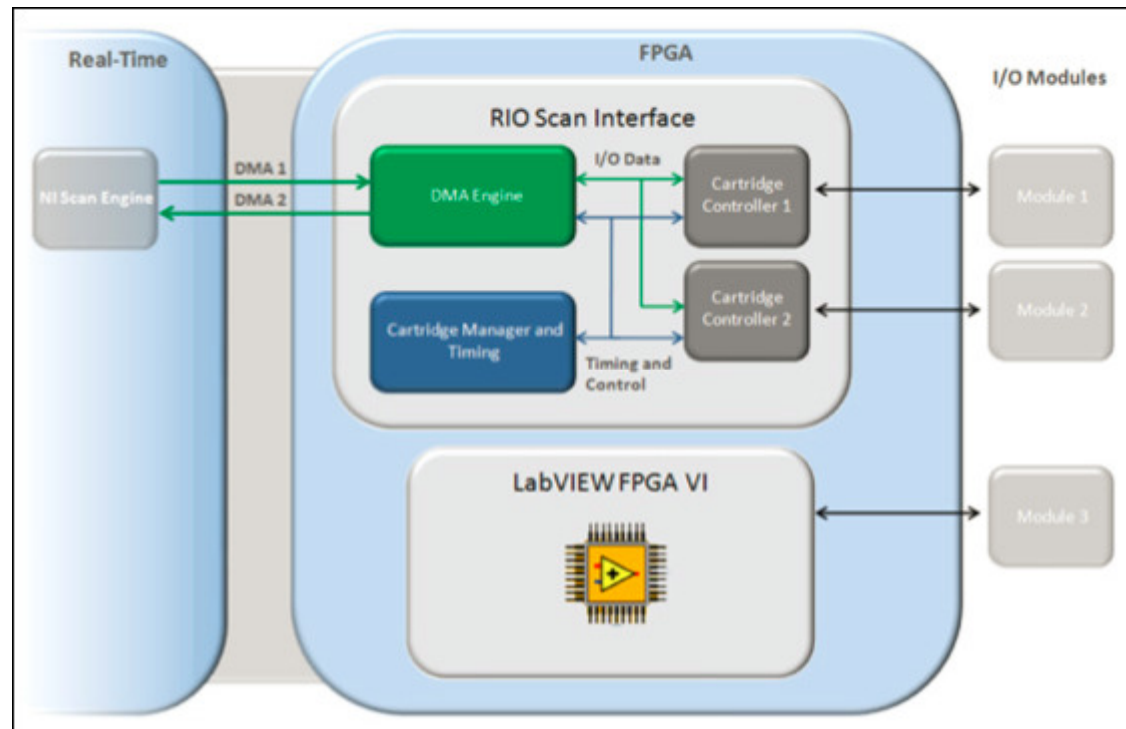
# Digital Output NI - 9472



# Motor Controller NI - 9505

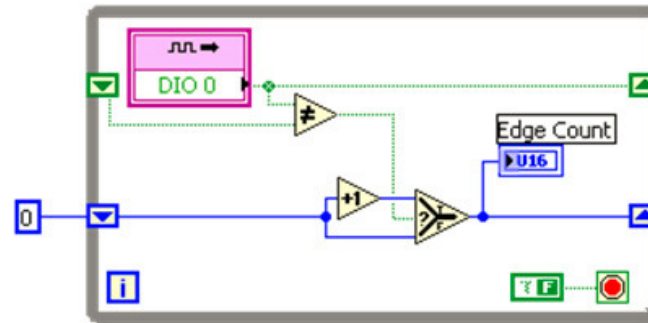


# FPGA Interface



<http://www.mit.bme.hu/system/files/oktatas/targyak/7258/criondevgudfull.pdf>

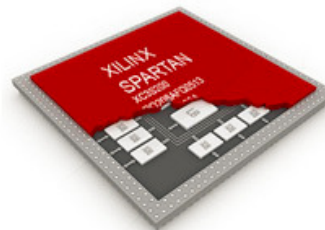
# Downloading LabVIEW Code to FPGA



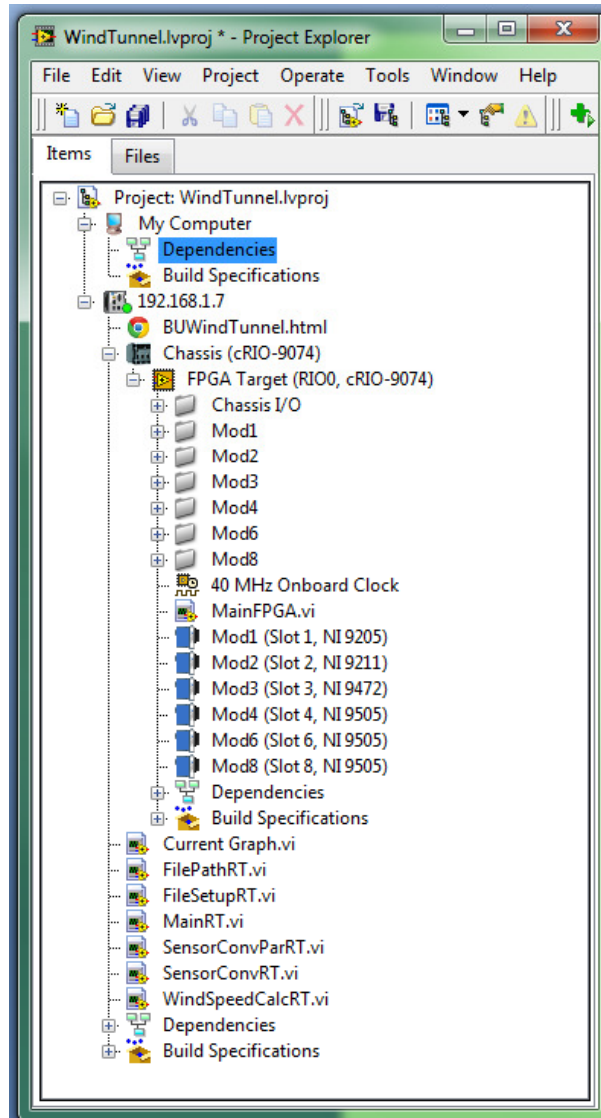
```
-- process synchronization
-- Then we keep track of what the digital input was on the previous
-- clock cycle by inserting another flip flop
previousdigitalinputFF:
process( areset, clk )
begin
  if areset then
    cprevdigitalinput <= false;
  elsif rising_edge(clk) then
    cprevdigitalinput <= cdigitalinput;
  end if;
end process PreviousdigitalinputFF;

-- Then we have a little combinatorial logic to detect a rising edge
risingEdgeDetected <= cdigitalinput and not cprevdigitalinput;

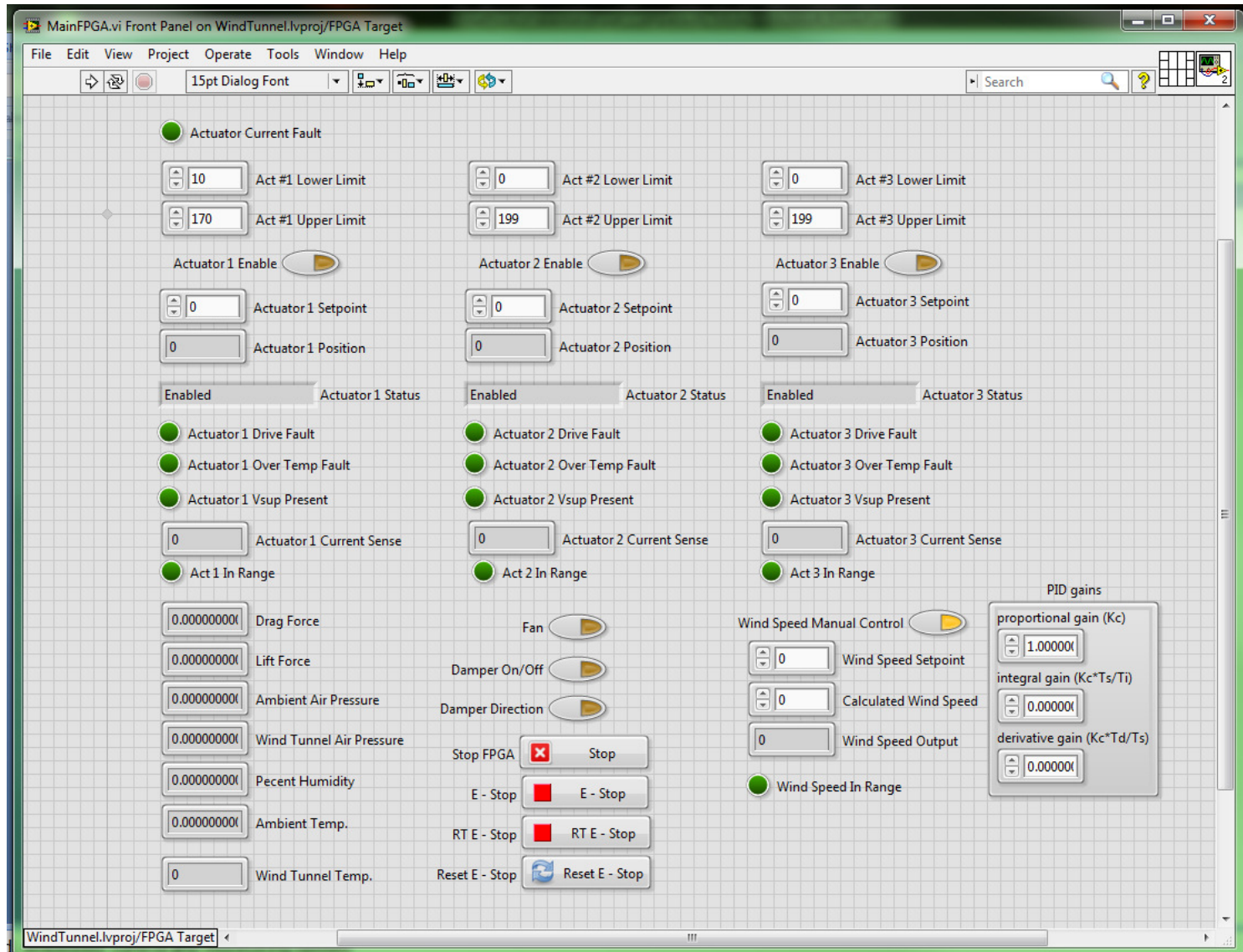
-- And finally we have a register that increments when that rising
-- edge is detected.
counterRegister:
process( areset, clk )
begin
  if areset then
    counterRegister <= 0;
  elsif rising_edge(clk) then
    counterRegister <= counterRegister + risingEdgeDetected;
  end if;
end process counterRegister;
```



# LabVIEW Project with FPGA / Real Time Controller / IO

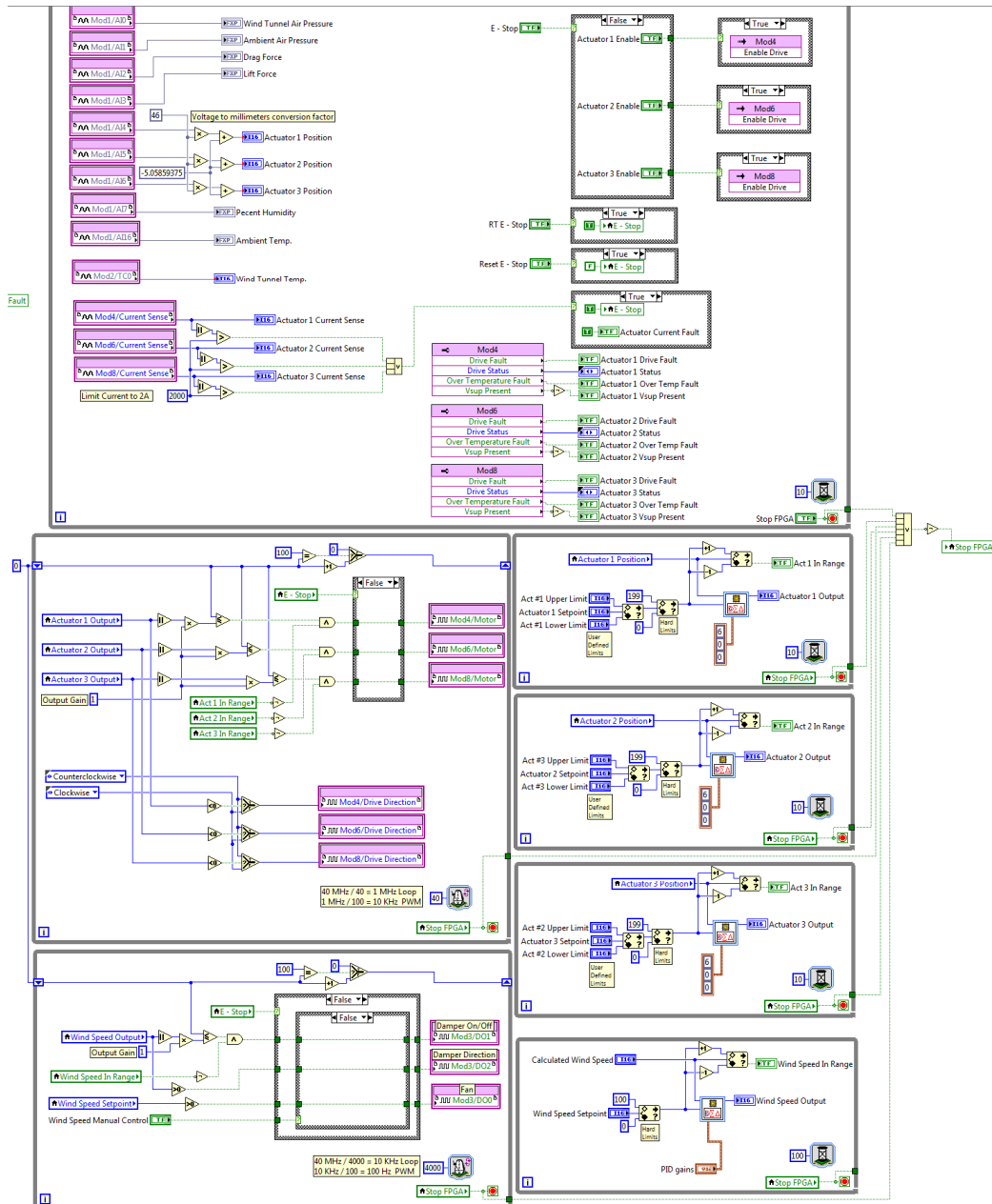


# FPGA Front Panel



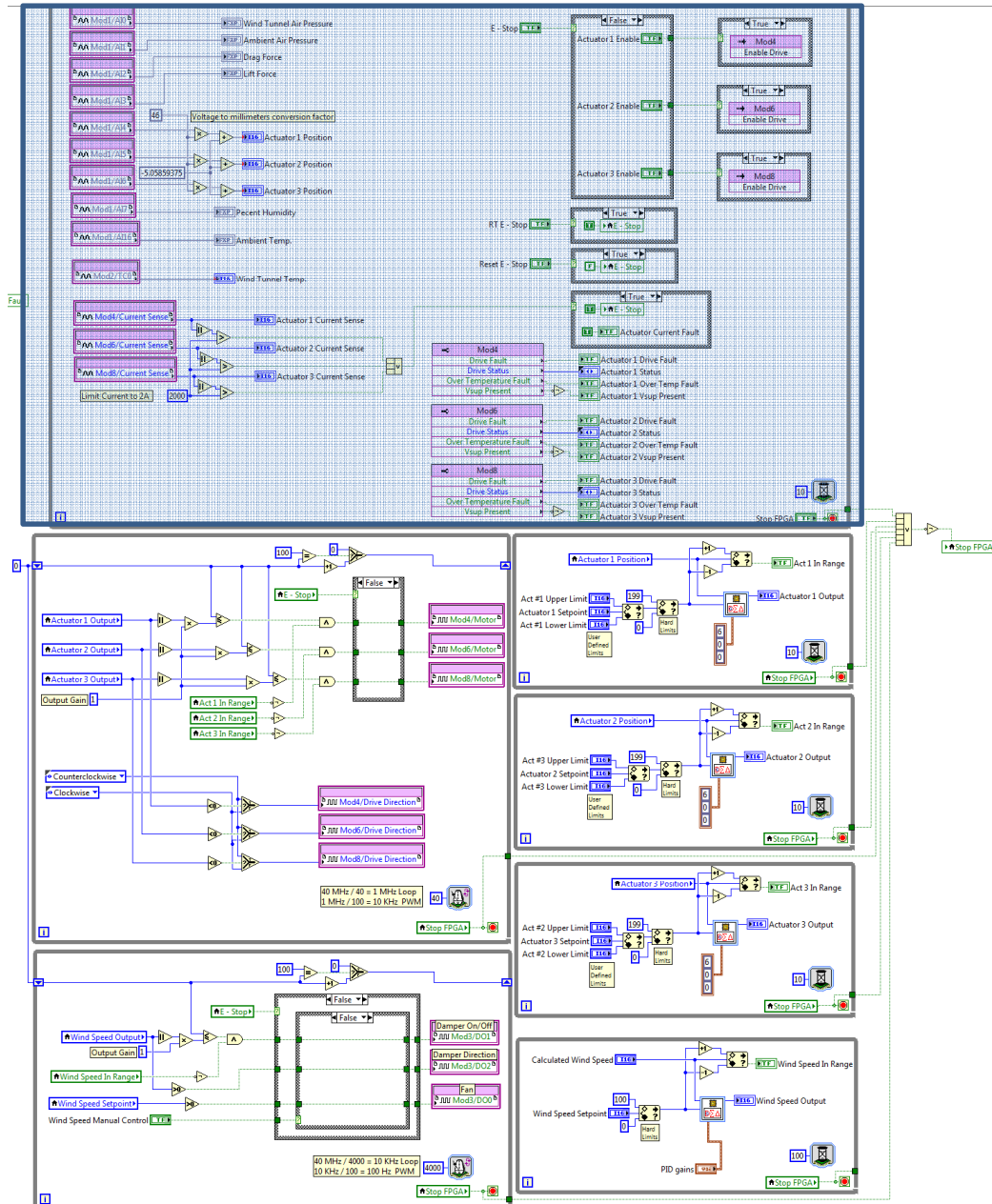
# FPGA LabVIEW Code

- Hardware I/O
- Actuator PID Control Loop
- Actuator Control PWM
- Wind Speed PID Control Loop
- Damper Control PWM



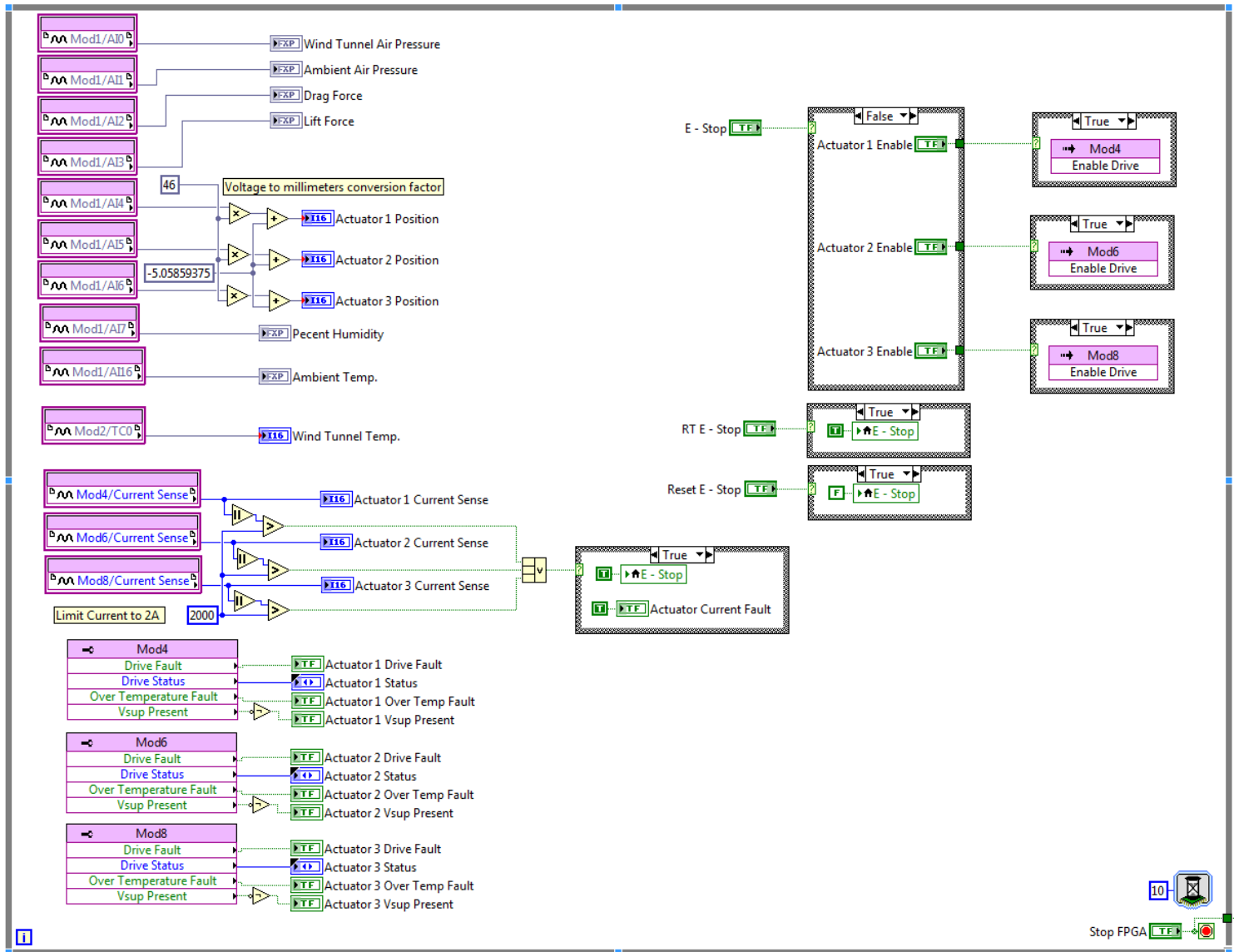
# FPGA LabVIEW Code

## Hardware I/O



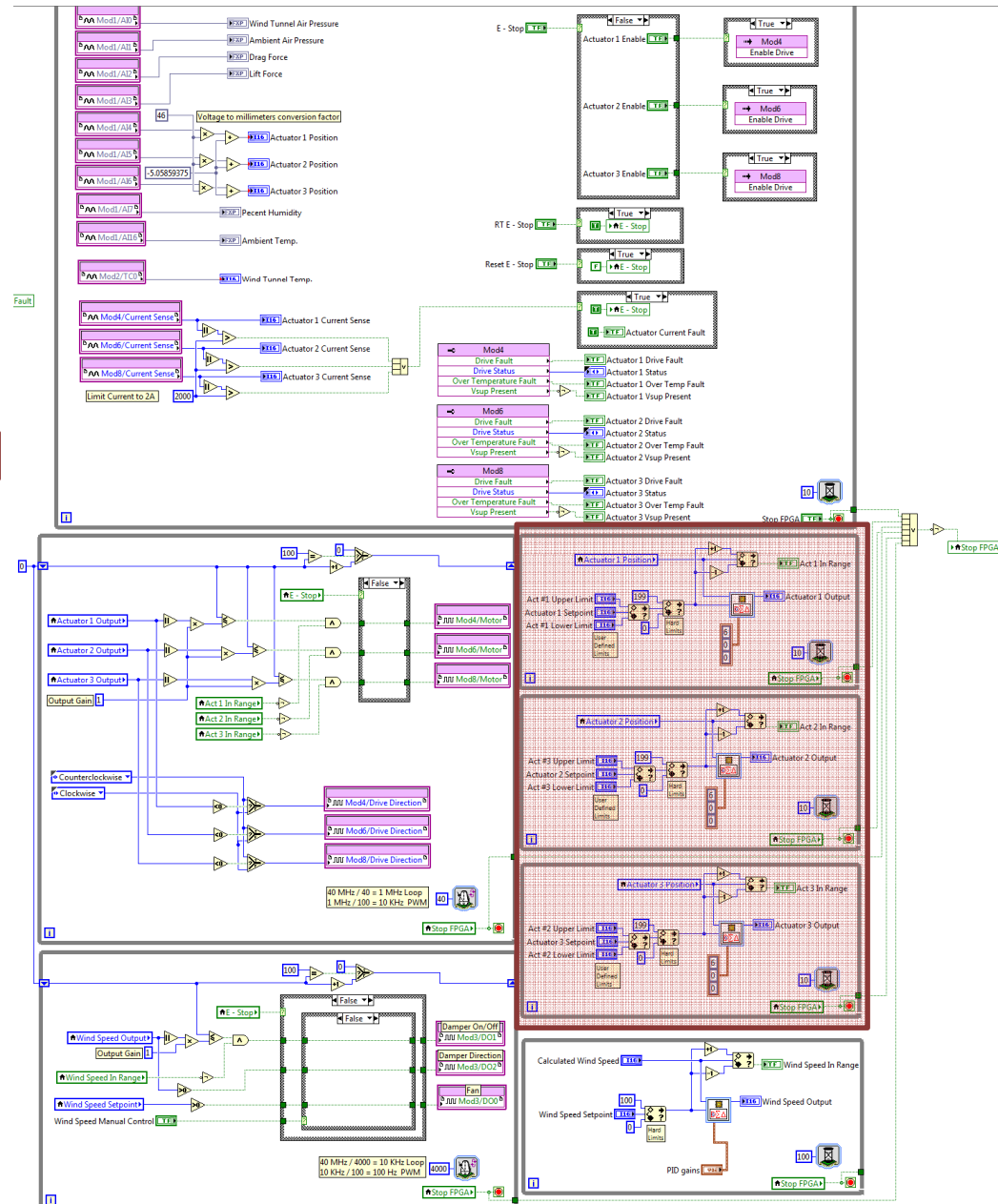


# Hardware I/O Interface With FPGA

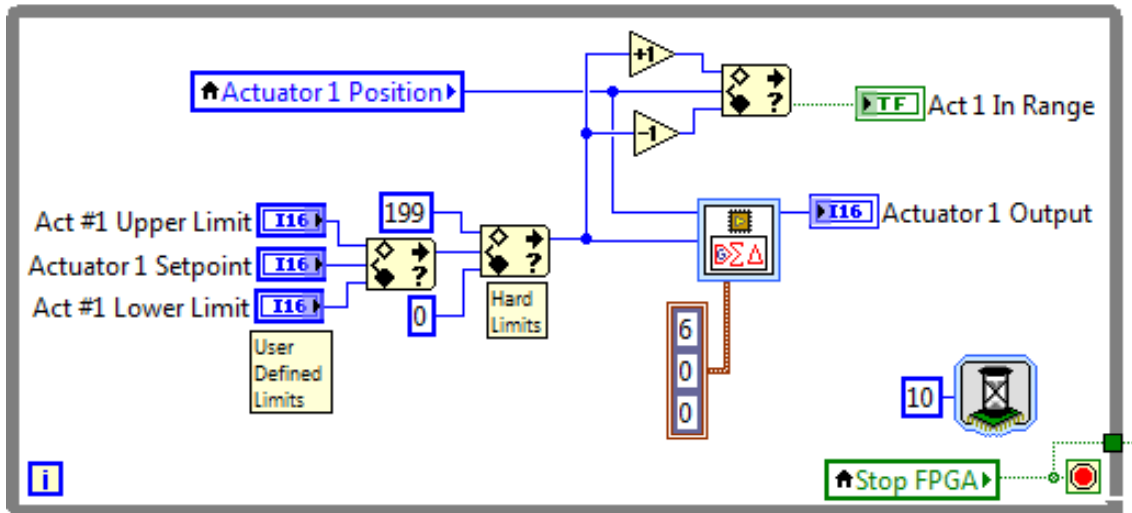


# FPGA LabVIEW Code

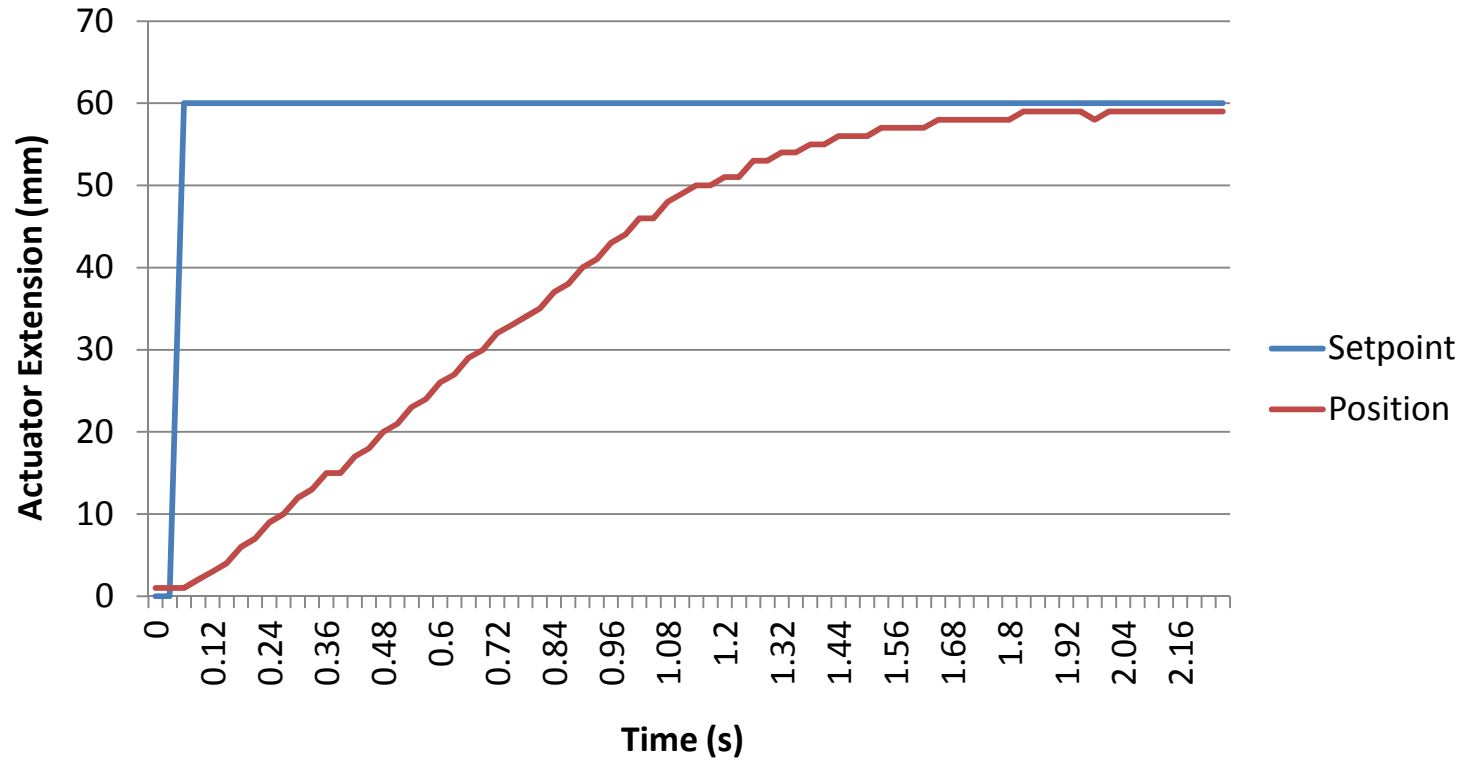
## Actuator PID Control Loop



# Actuator PID Control Loop

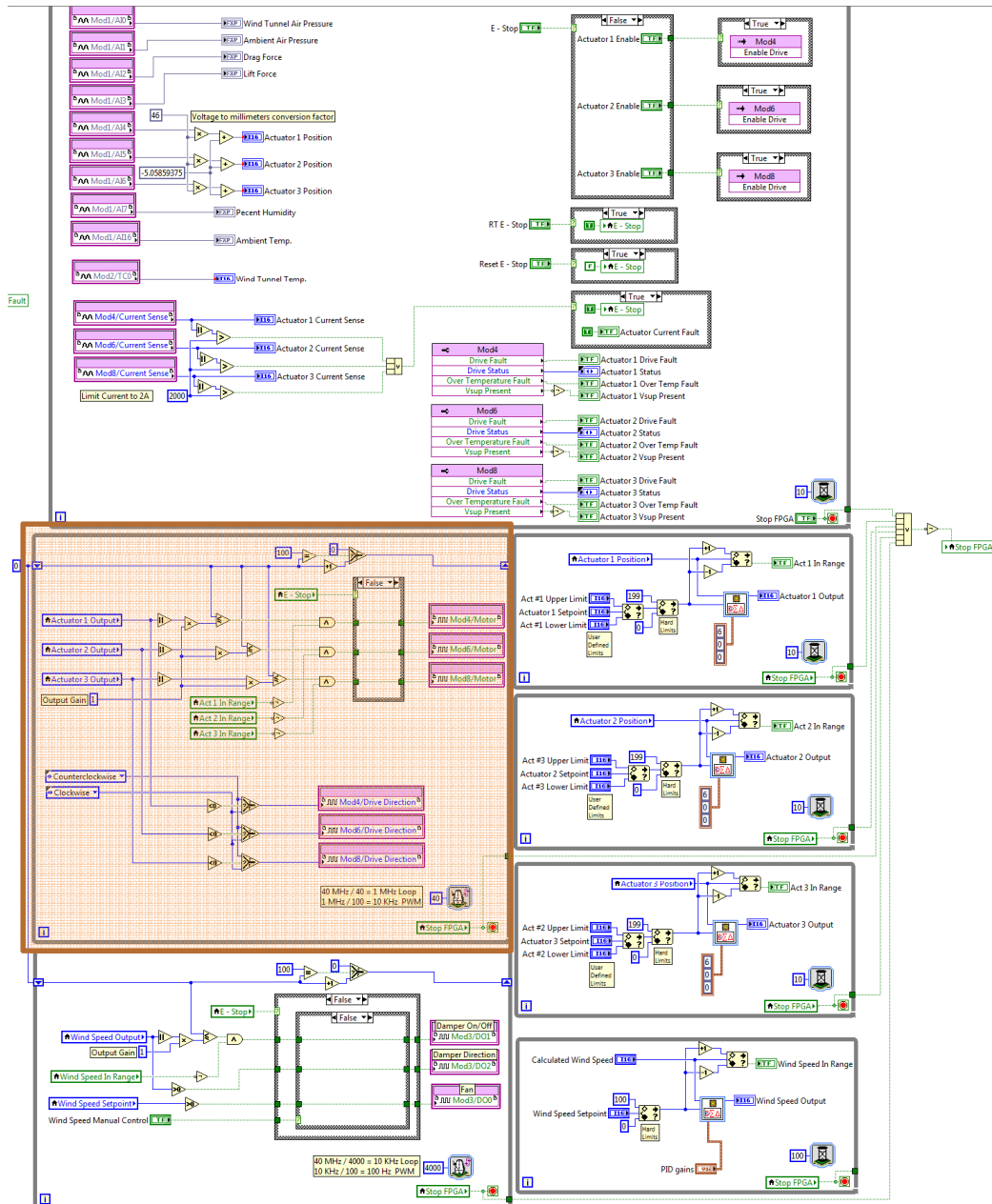


# Linear Actuator Distance Versus Setpoint

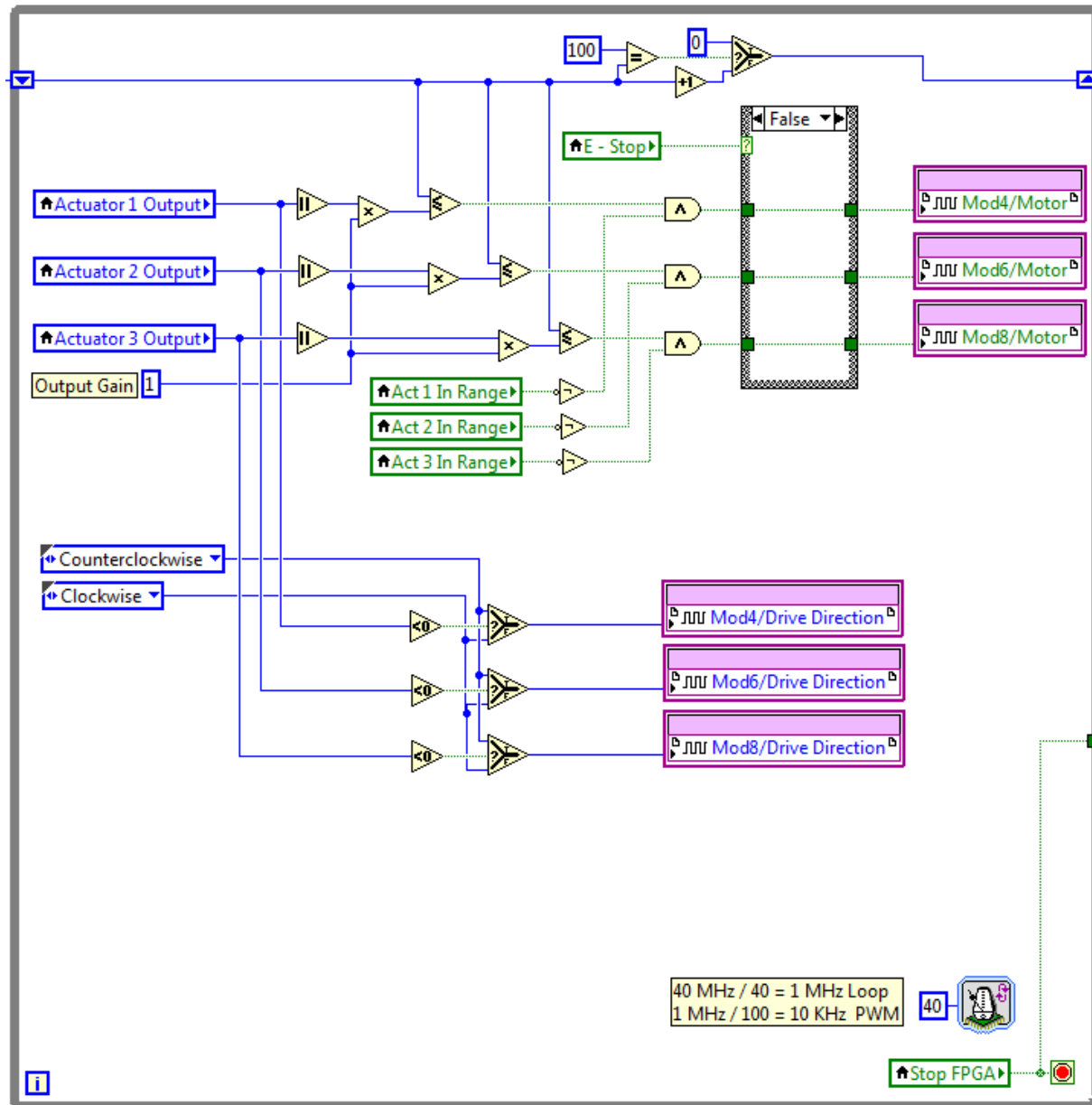


# FPGA LabVIEW Code

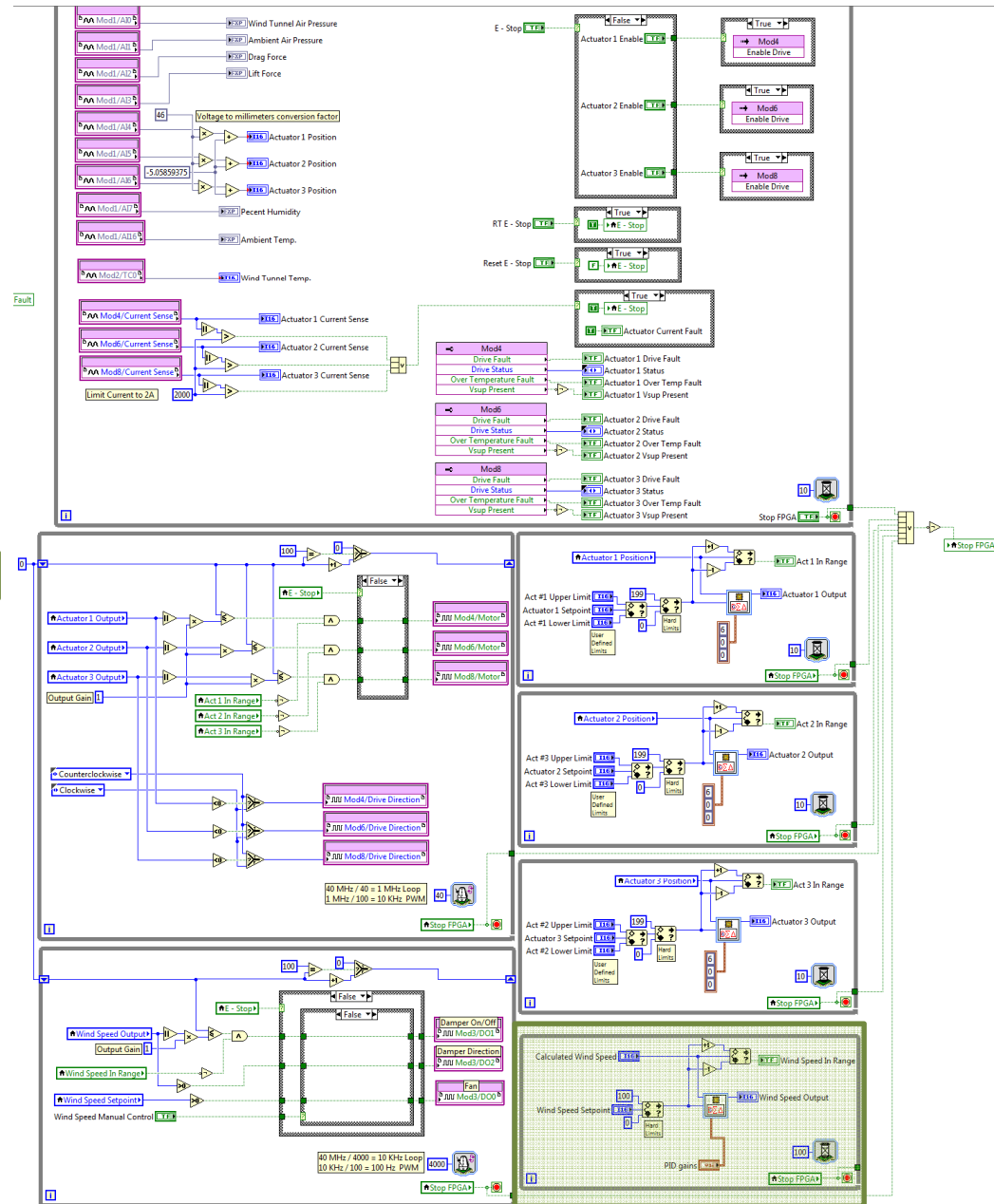
## Actuator Control PWM



# Actuator Control PWM Generation

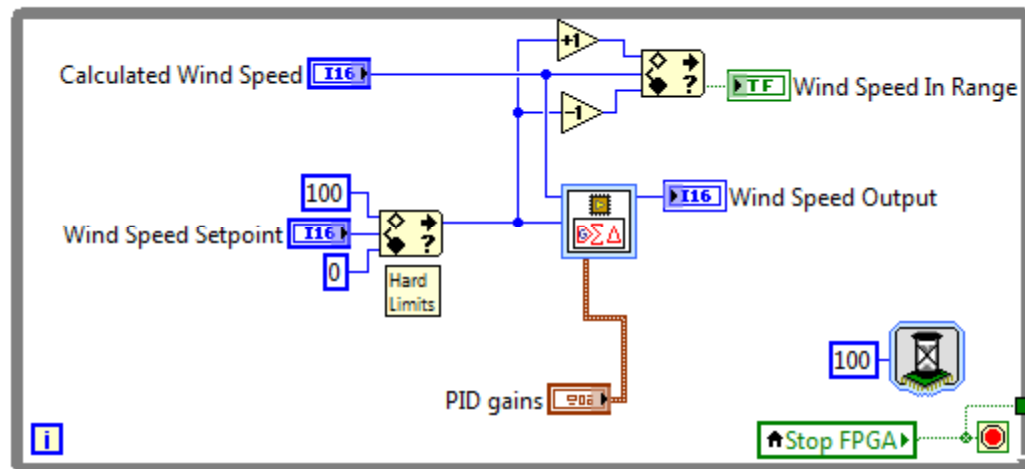


# FPGA LabVIEW Code



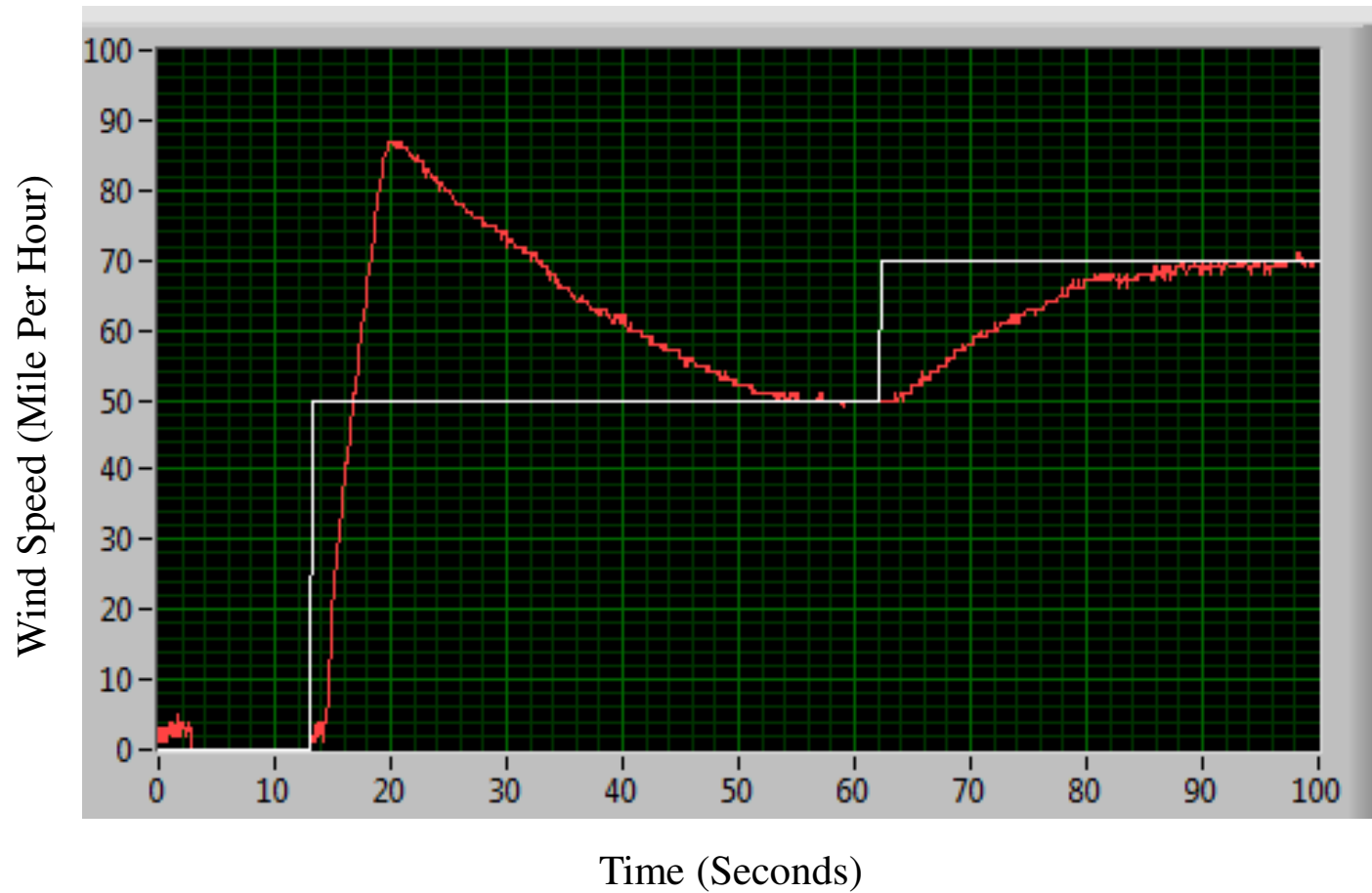
Wind Speed PID Control Loop

# Wind Speed PID Control Loop

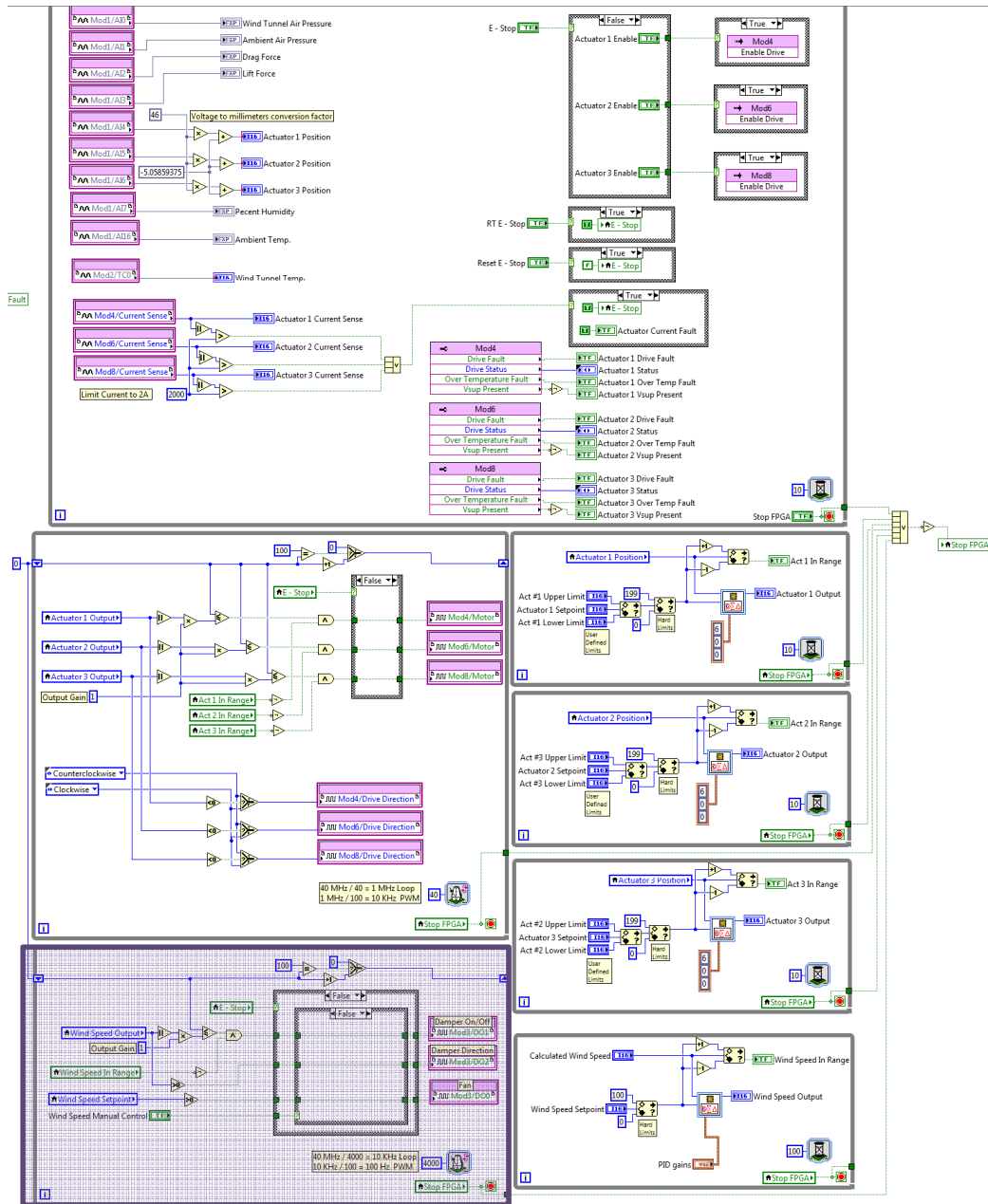




# Wind Speed Versus Setpoint

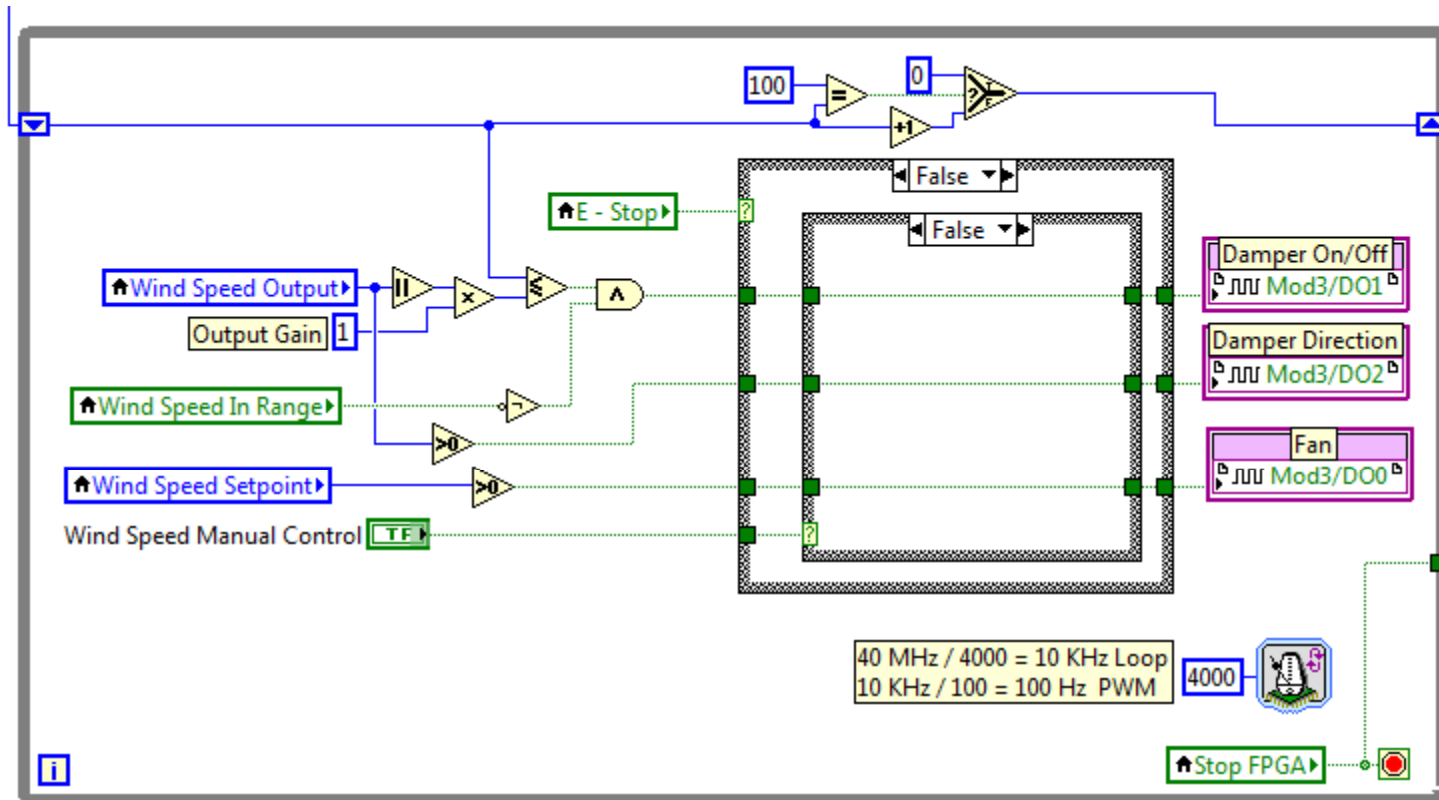


# FPGA LabVIEW Code



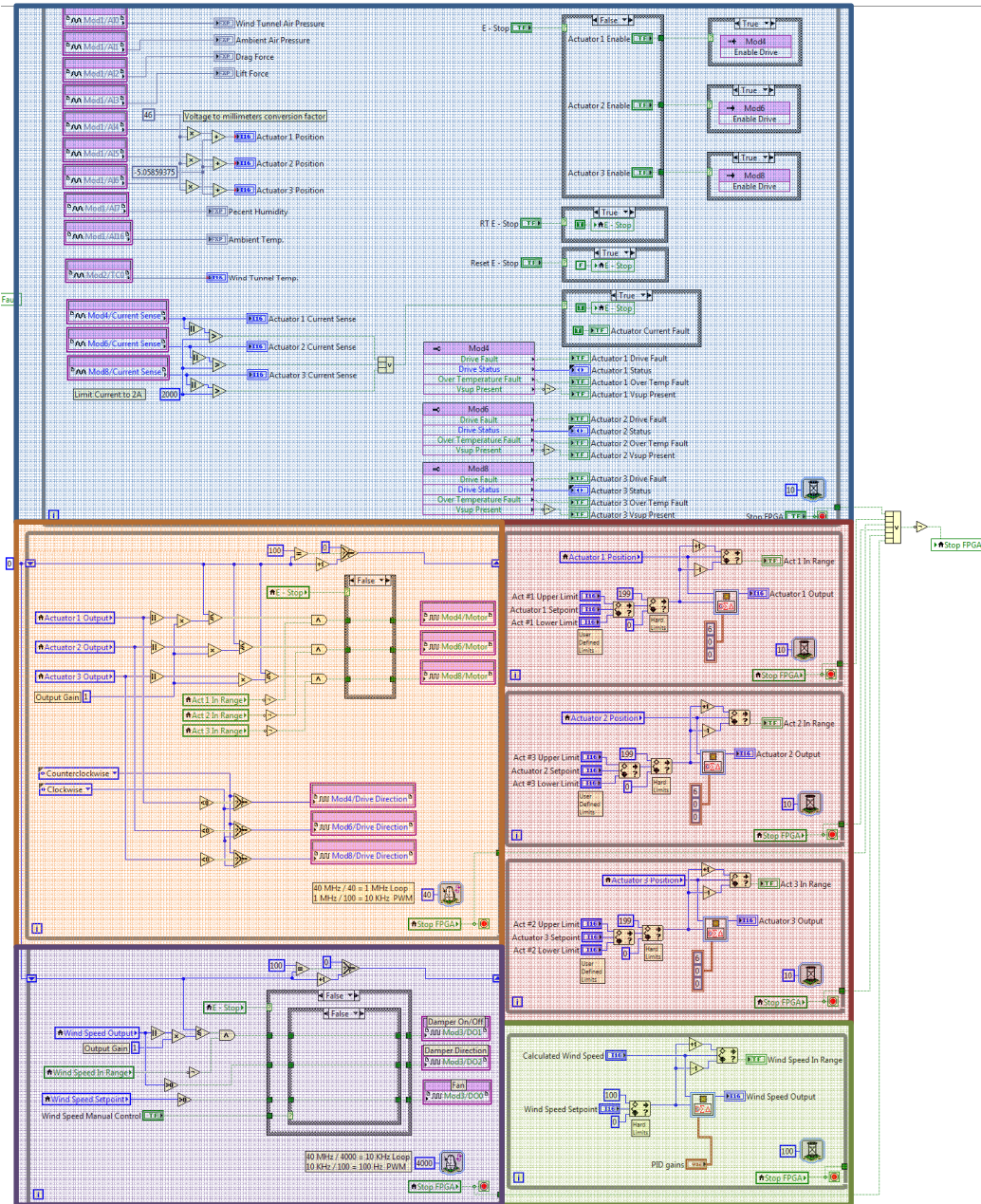
## Damper Control PWM

# Damper Control PWM Generation

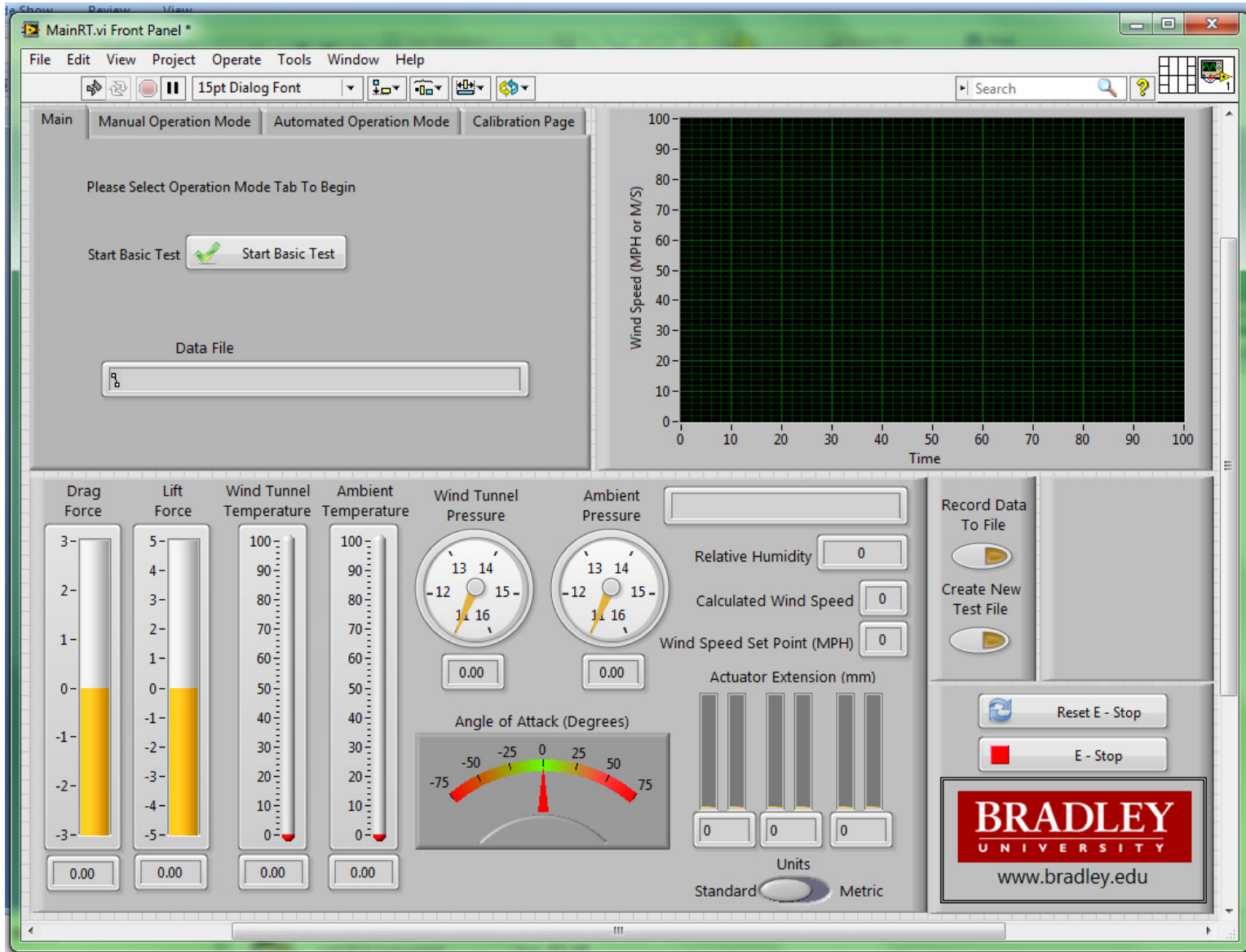


# FPGA LabVIEW Code

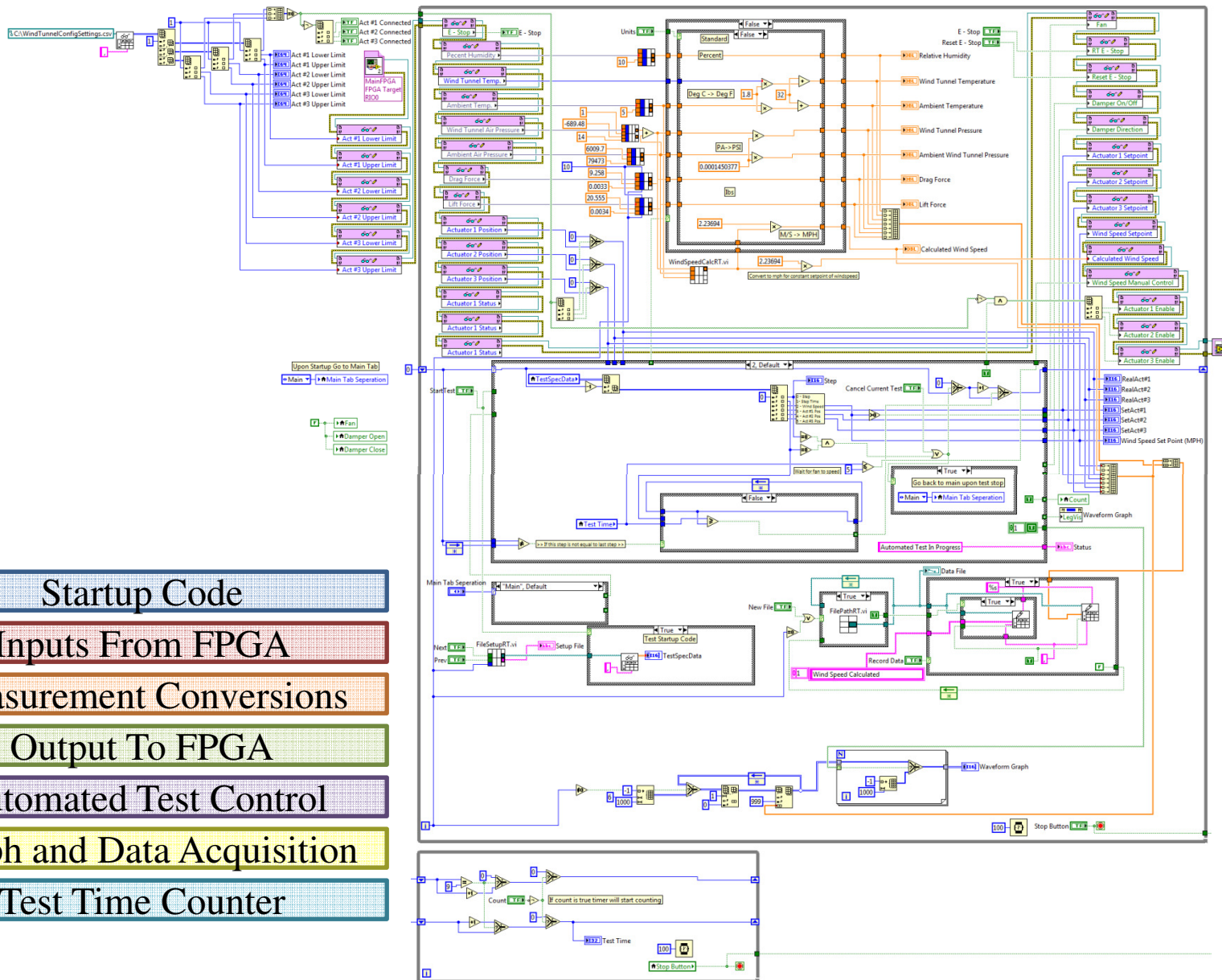
- Hardware I/O
- Actuator PID Control Loop
- Actuator Control PWM
- Wind Speed PID Control Loop
- Damper Control PWM



# Real Time Target LabVIEW Code

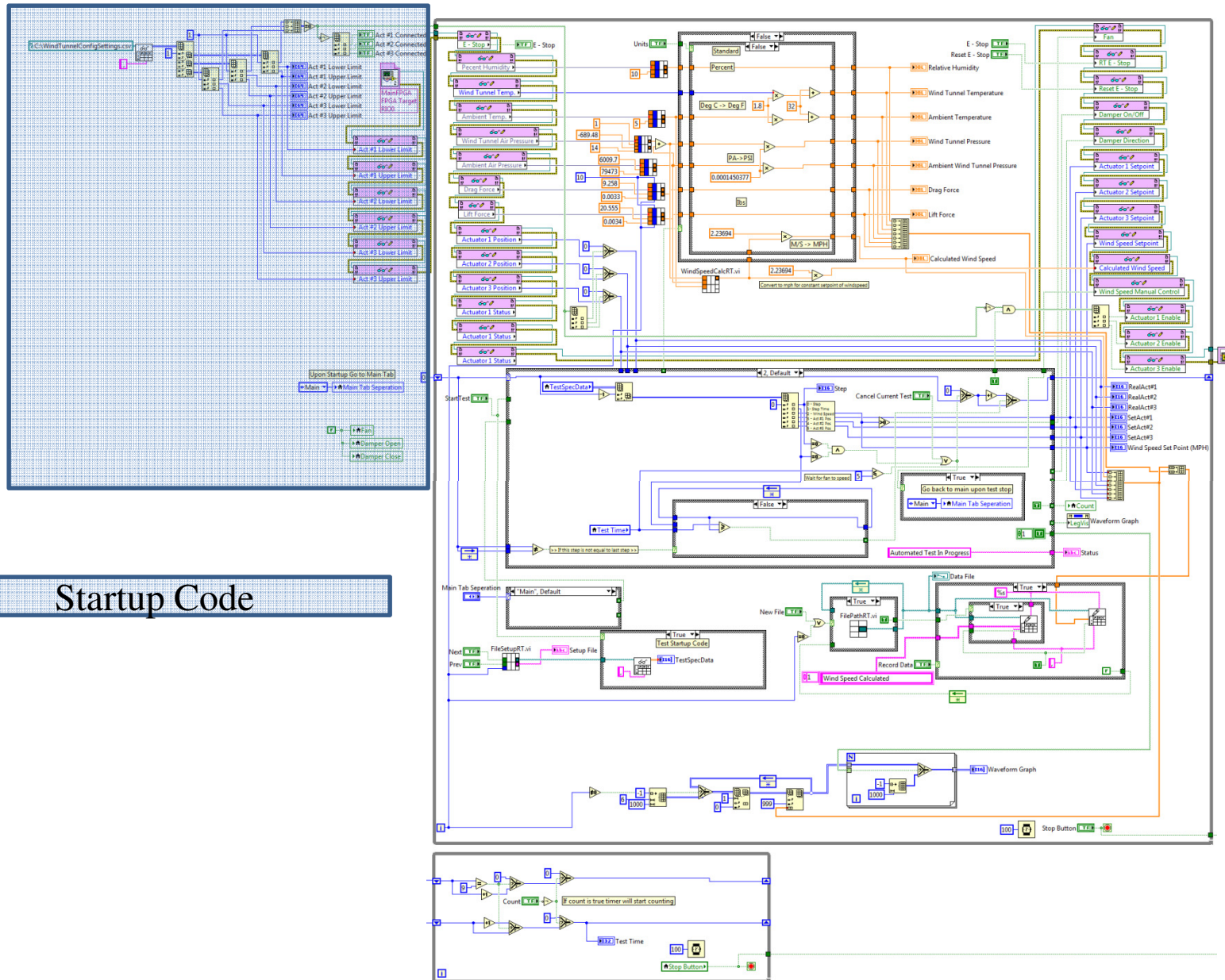


# Real Time Target LabVIEW Code

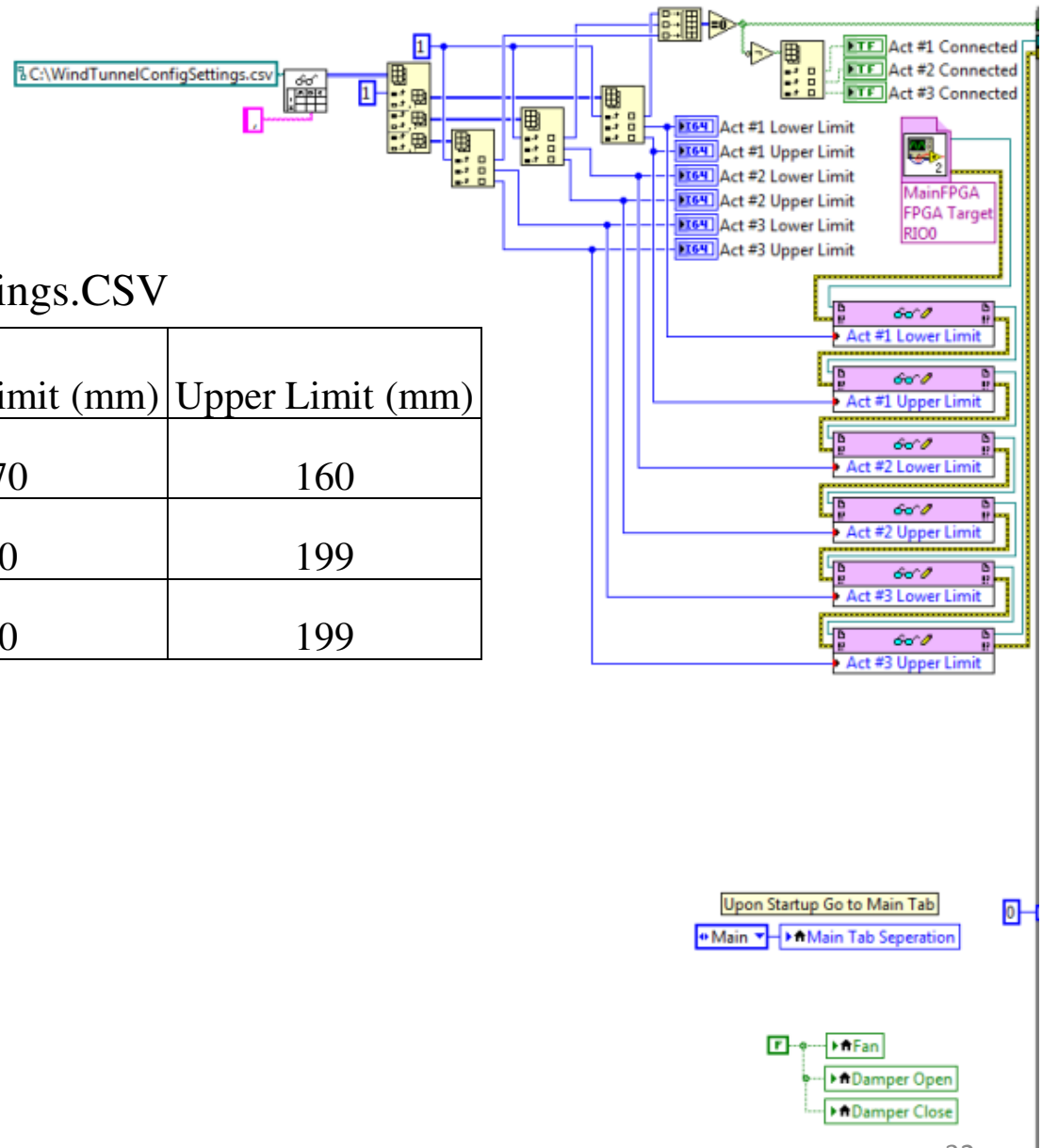


- Startup Code
- Inputs From FPGA
- Measurement Conversions
- Output To FPGA
- Automated Test Control
- Graph and Data Acquisition
- Test Time Counter

# Real Time Target LabVIEW Code



# Startup Code

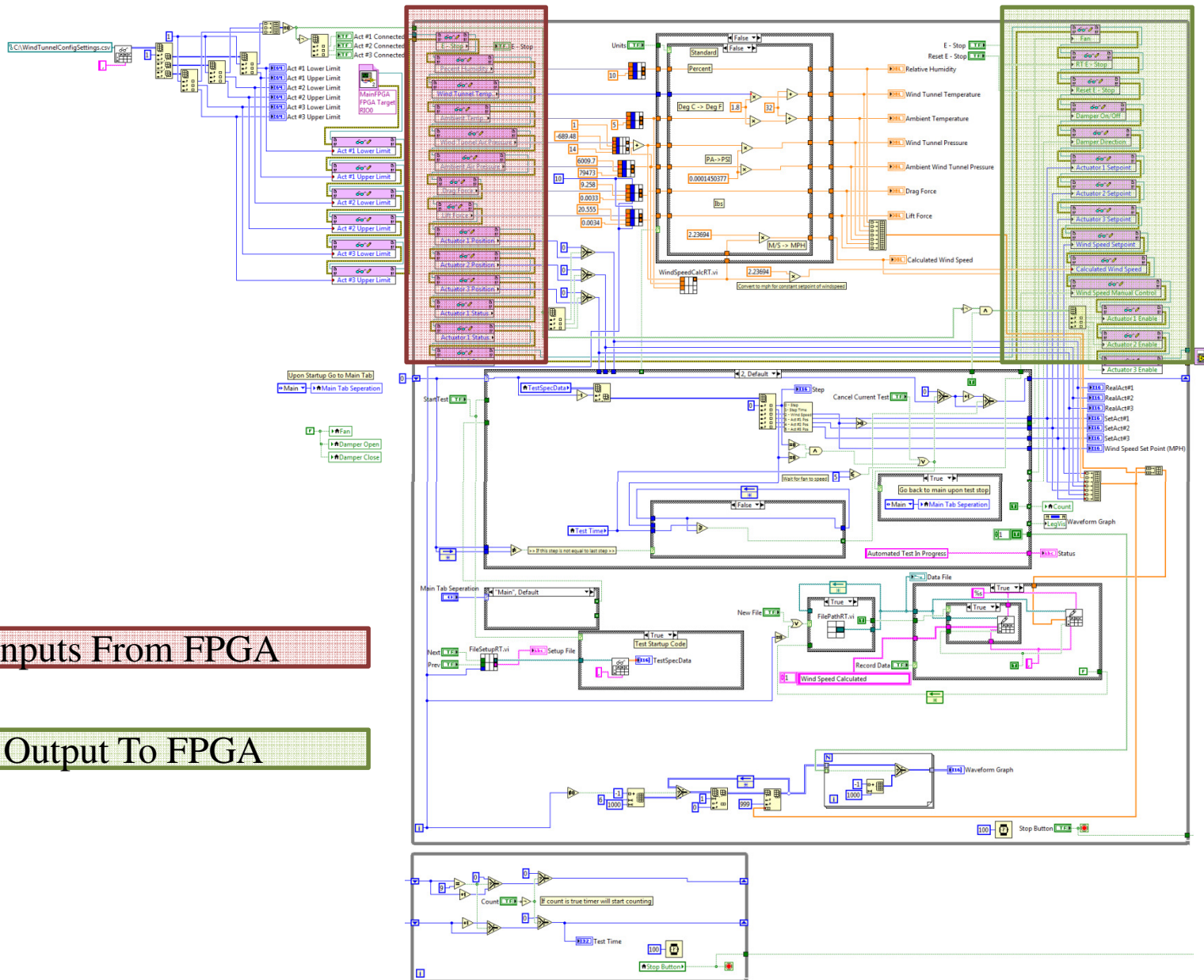


WindTunnelConfigSettings.CSV

	Enable(1)/Disable(0)	Lower Limit (mm)	Upper Limit (mm)
Actuator #1	1	70	160
Actuator #2	1	0	199
Actuator #3	0	0	199



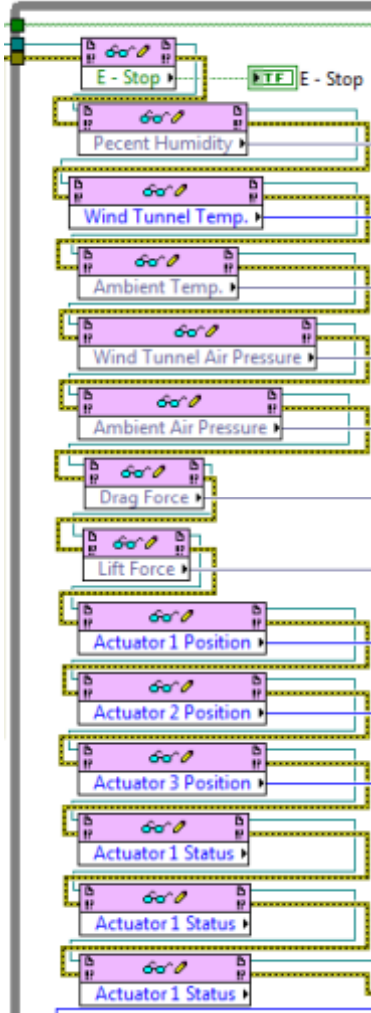
# Real Time Target LabVIEW Code



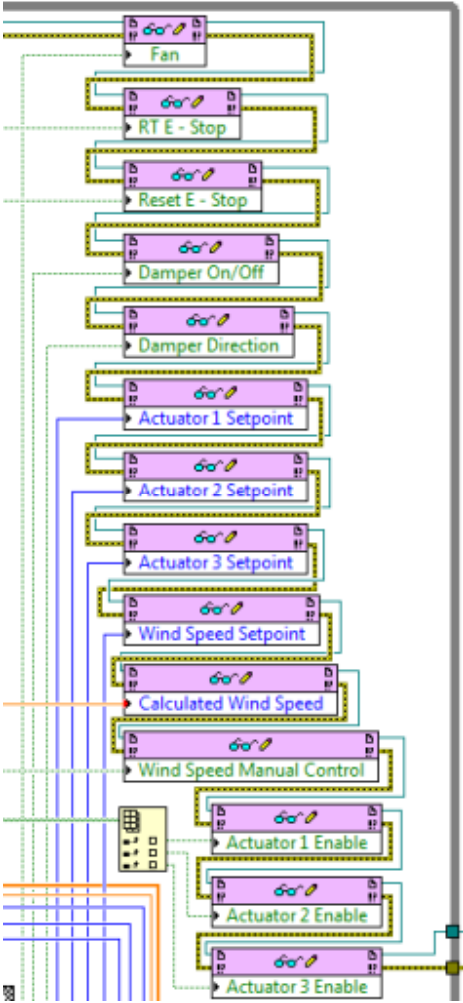
Inputs From FPGA

Output To FPGA

# FPGA I/O

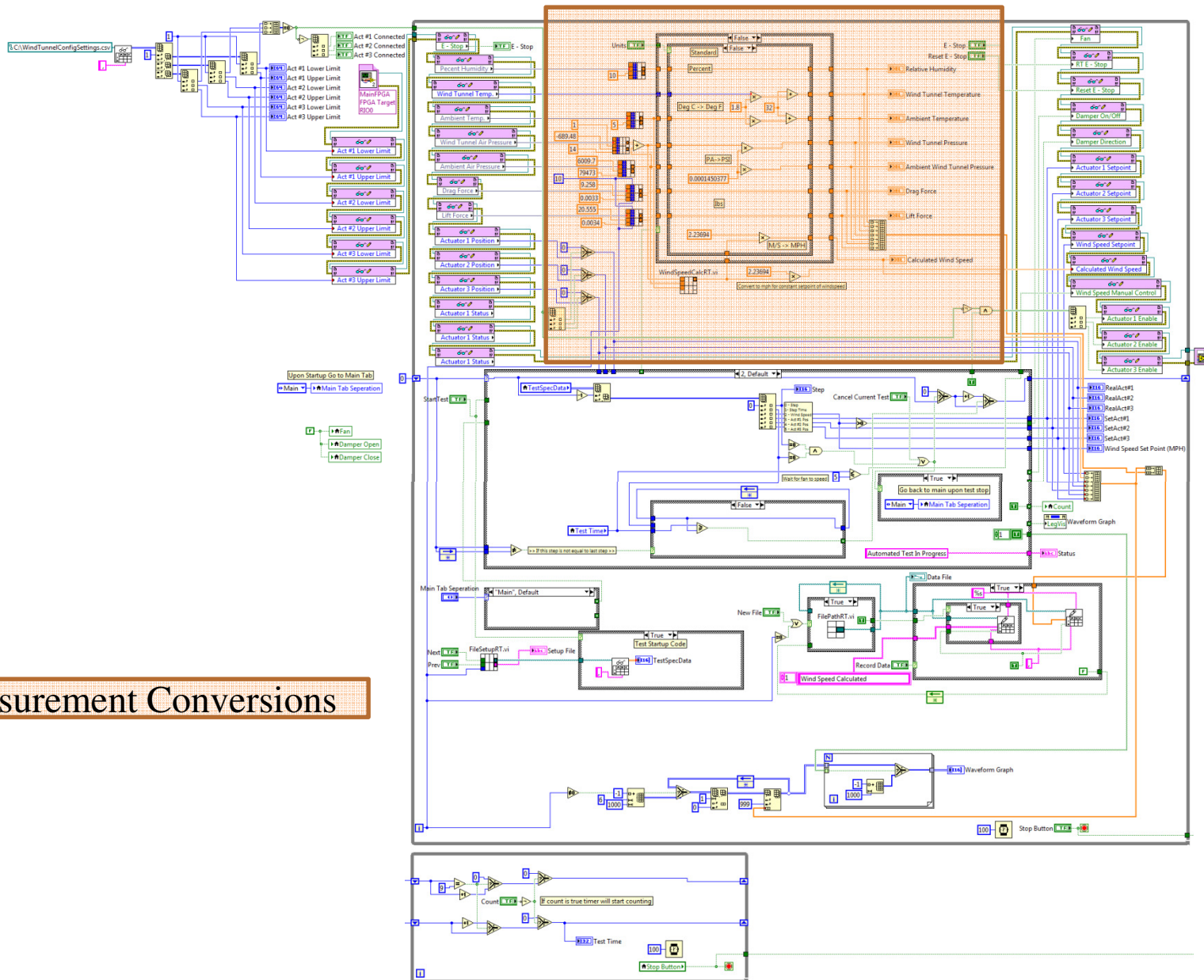


Inputs From FPGA

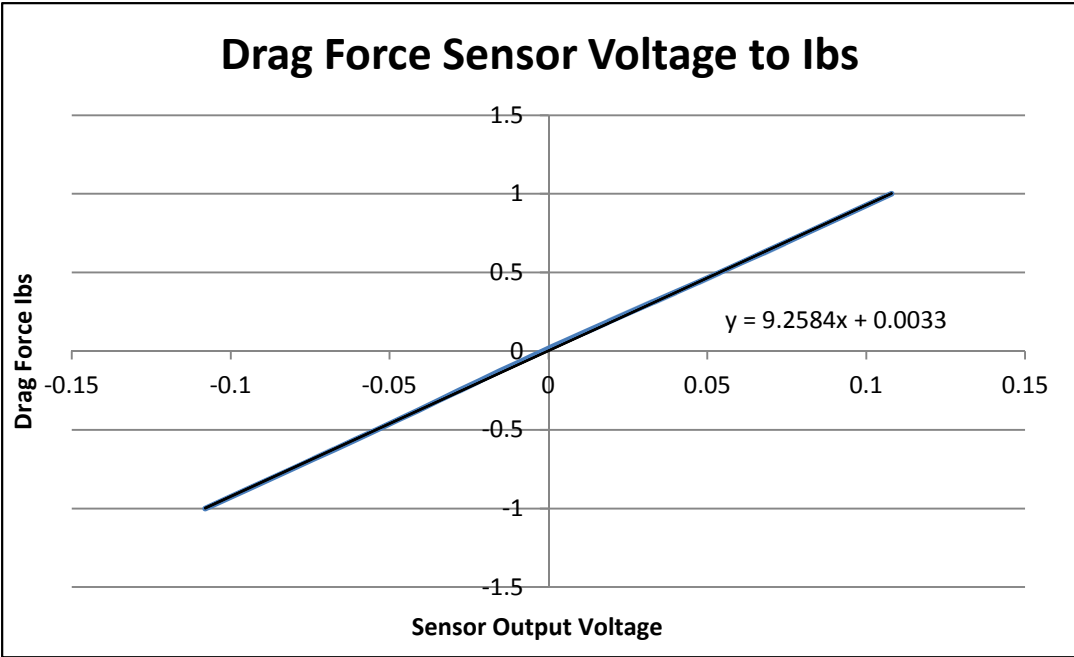
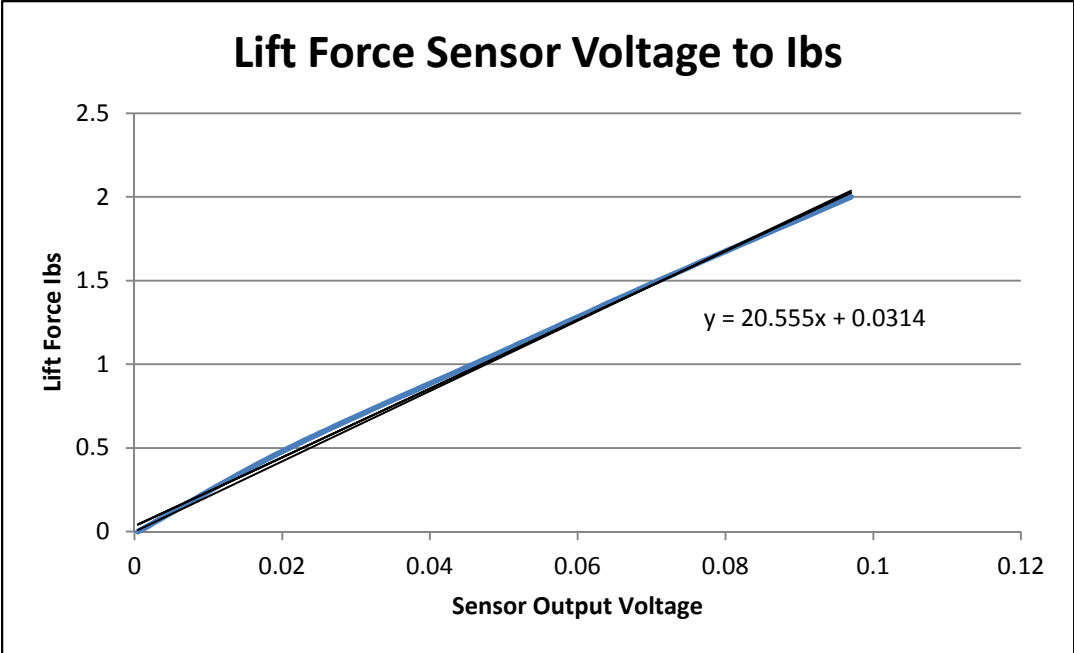


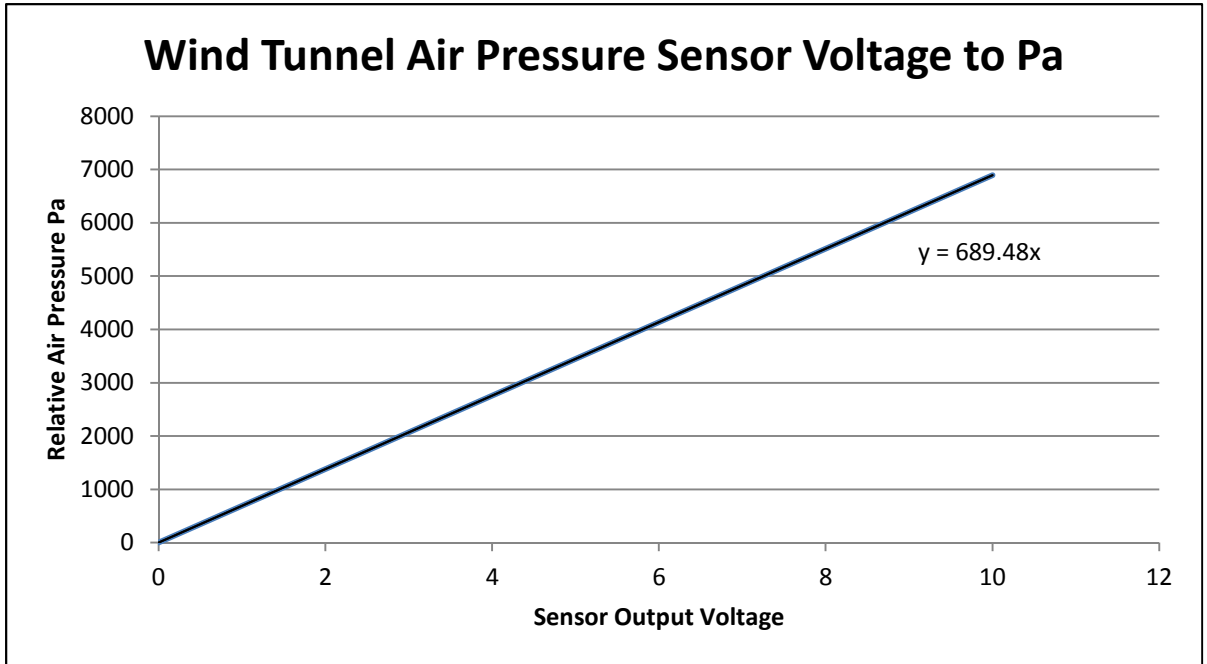
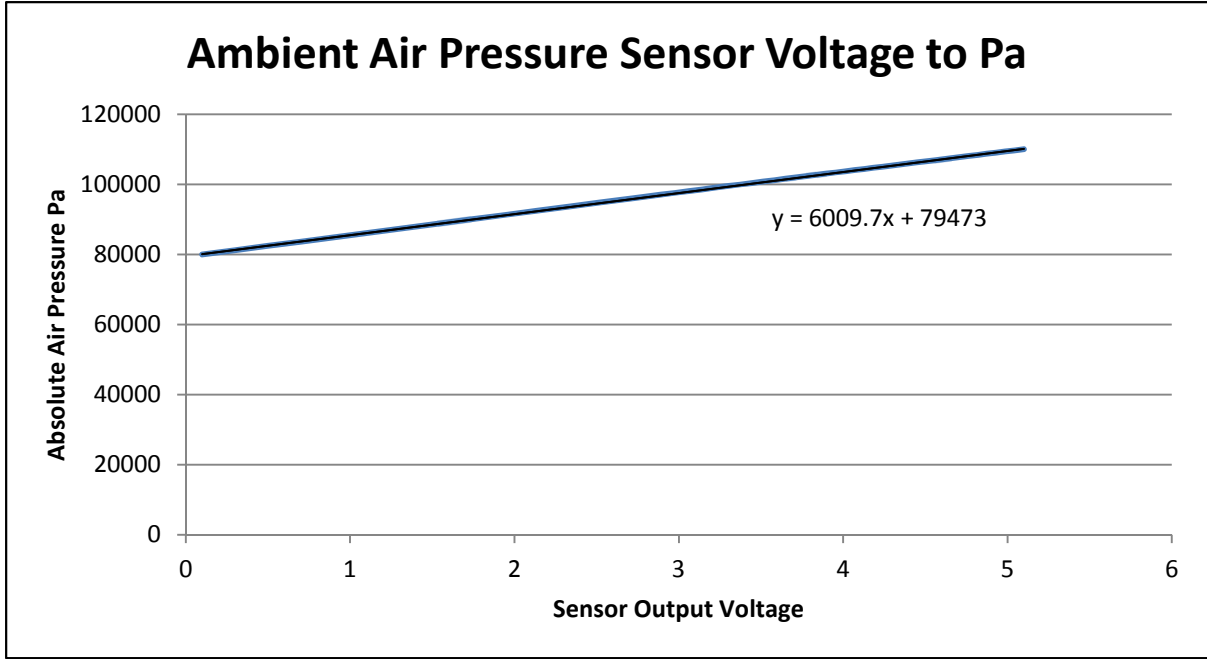
Outputs To FPGA

# Real Time Target LabVIEW Code

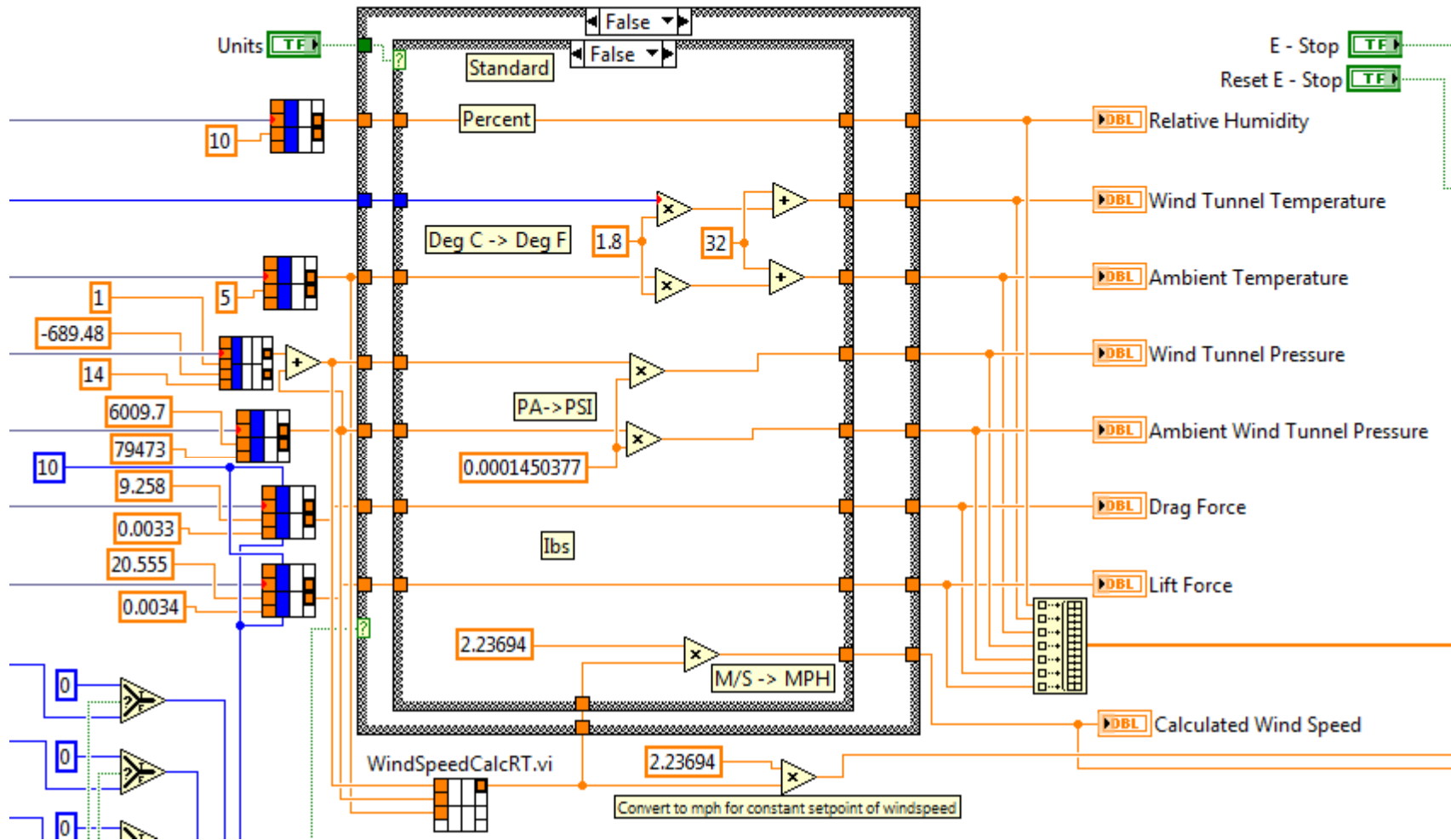


Measurement Conversions

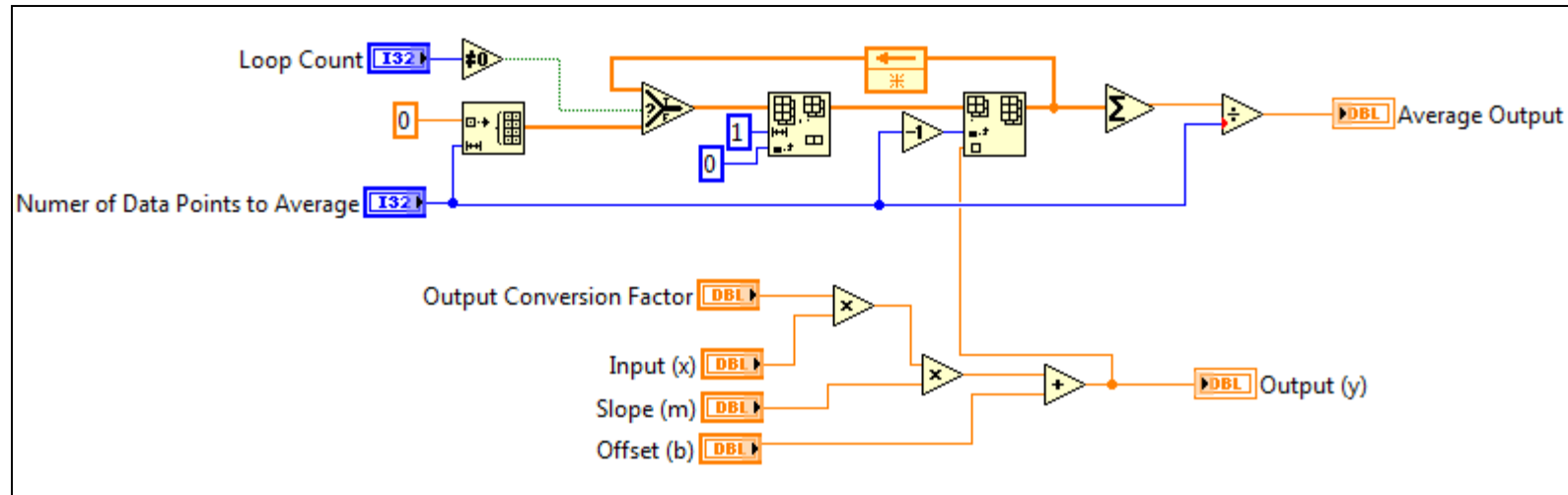




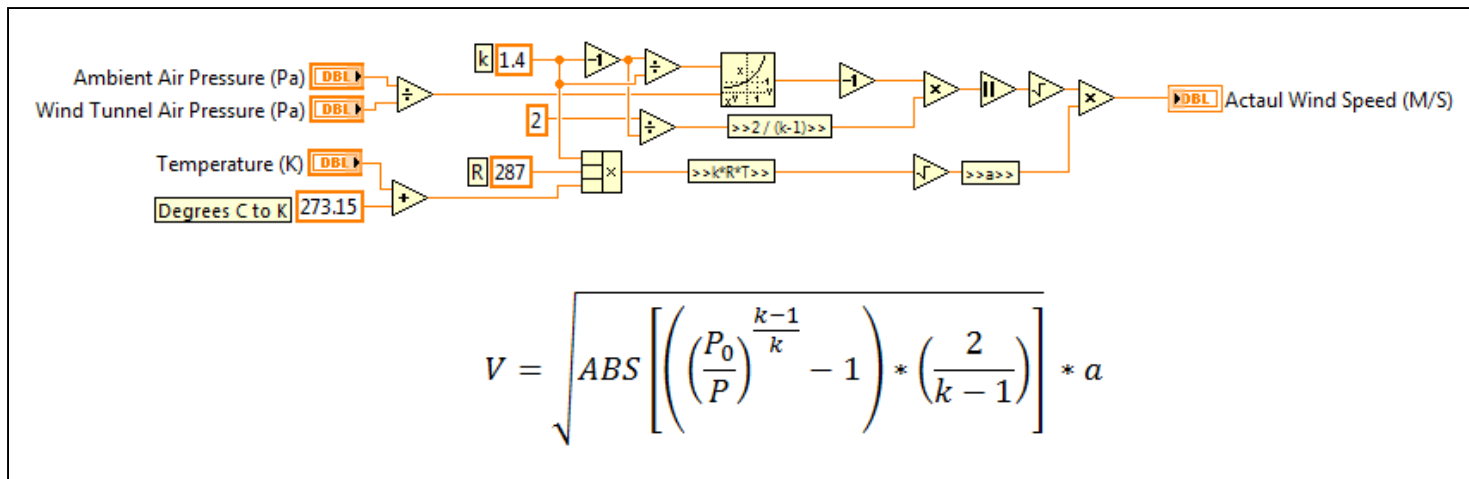
# Measurement Conversions



# Measurement Conversion Sub VIs

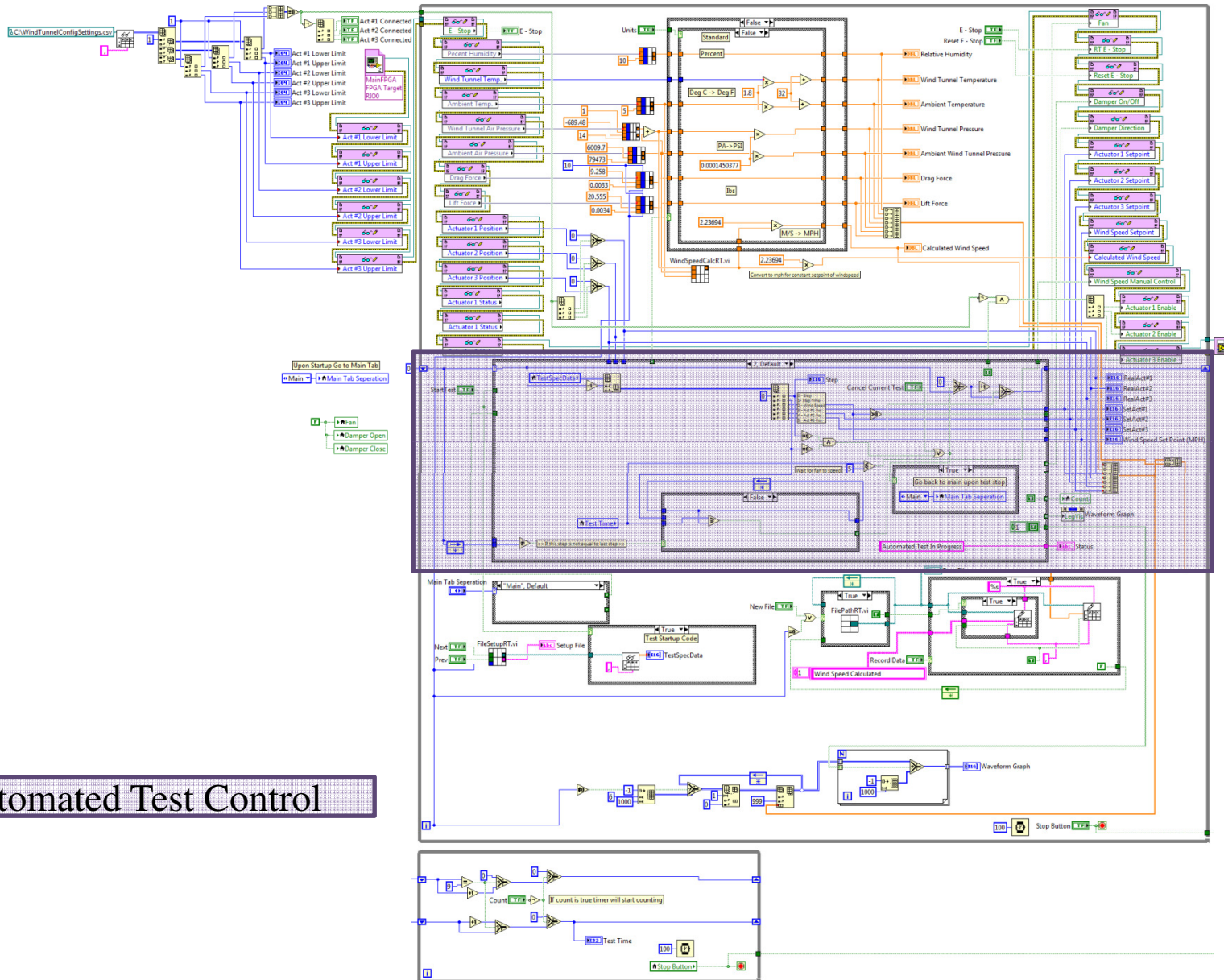


SensorConvRT.VI



WindSpeedCalcRT.VI

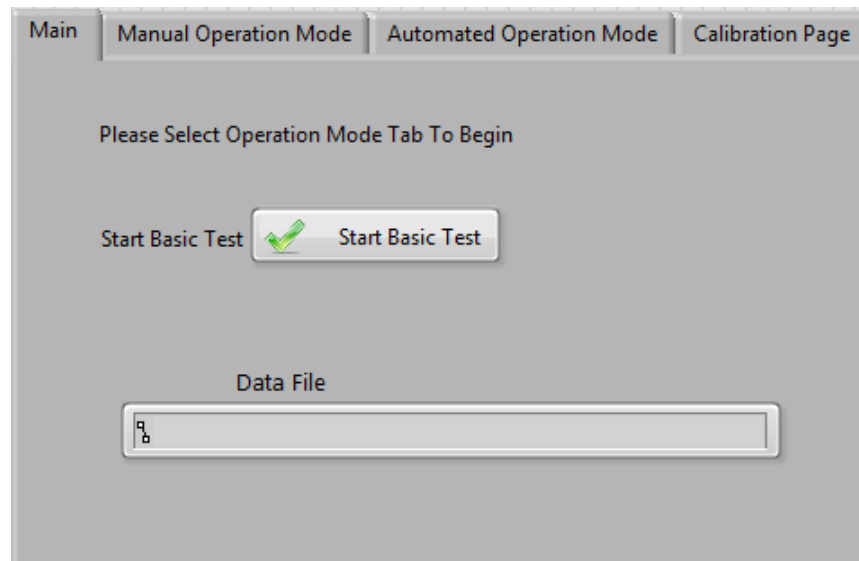
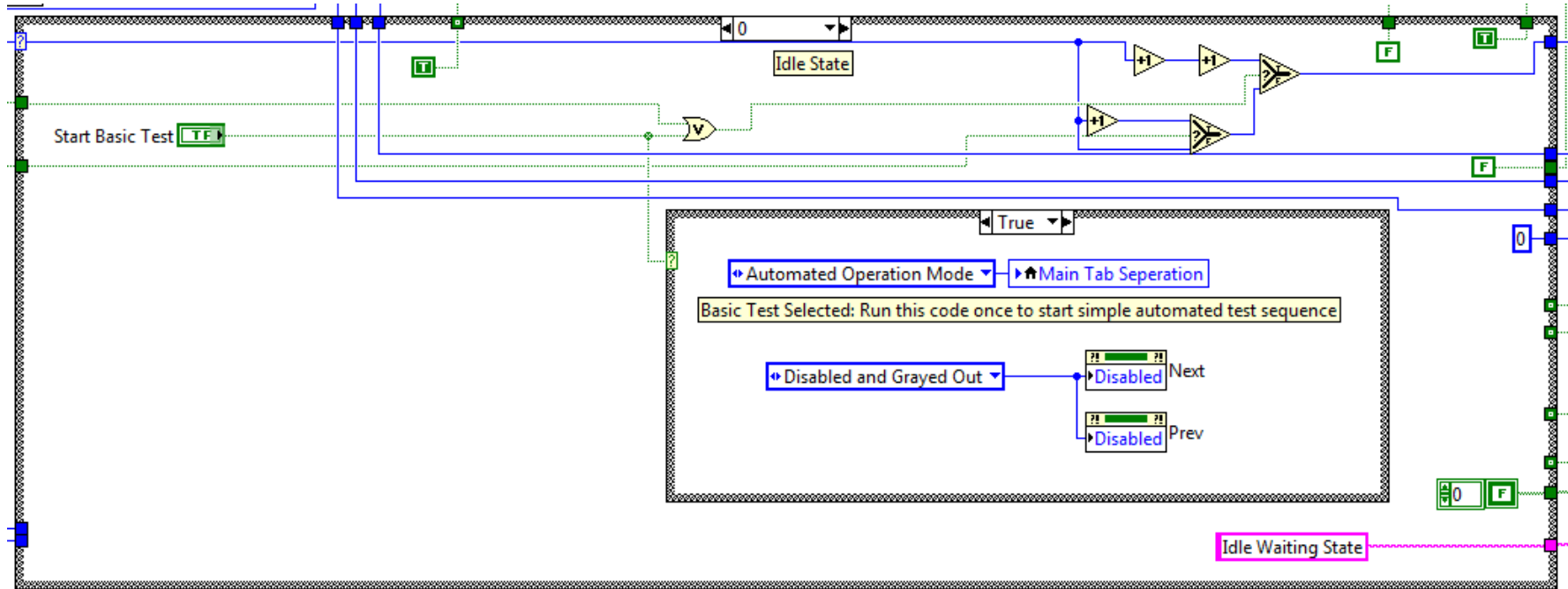
# Real Time Target LabVIEW Code



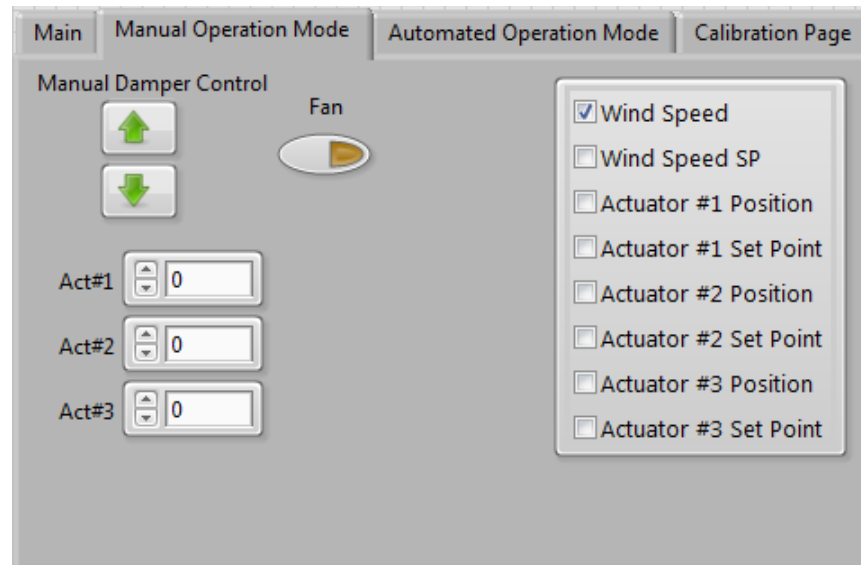
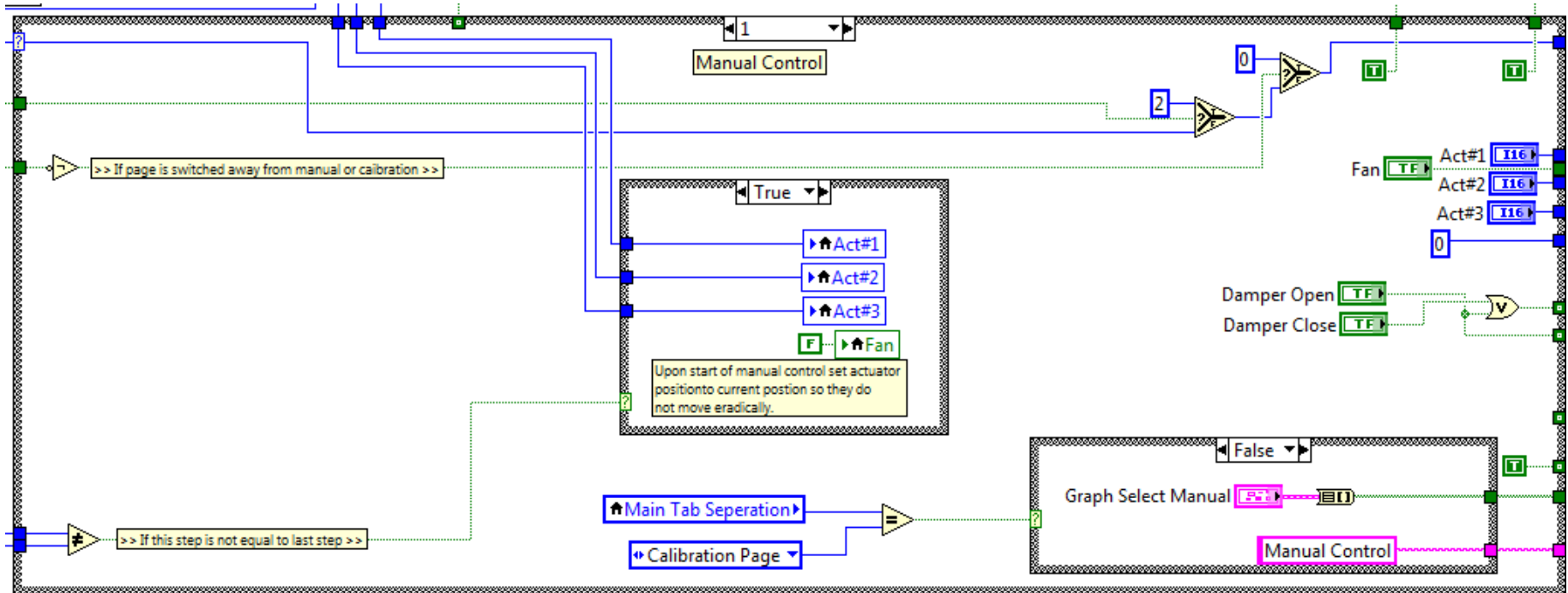
Automated Test Control



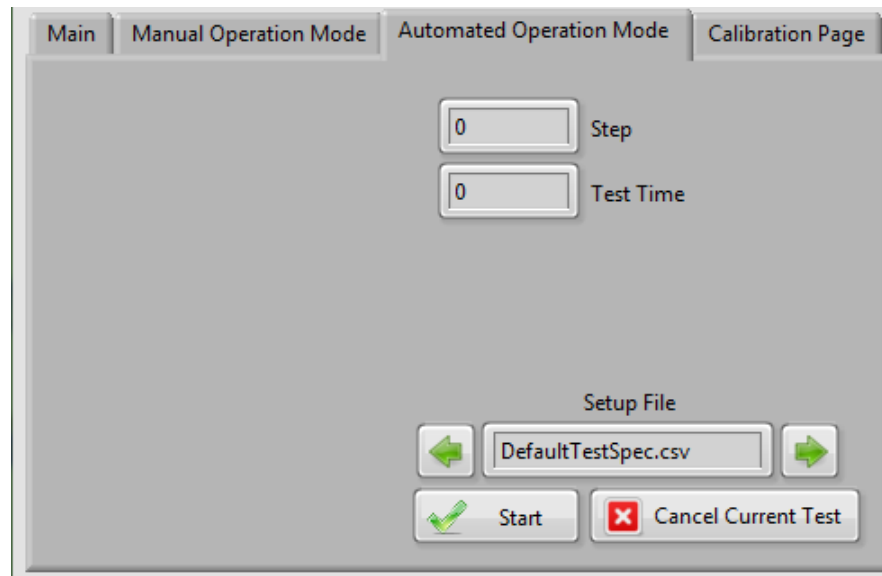
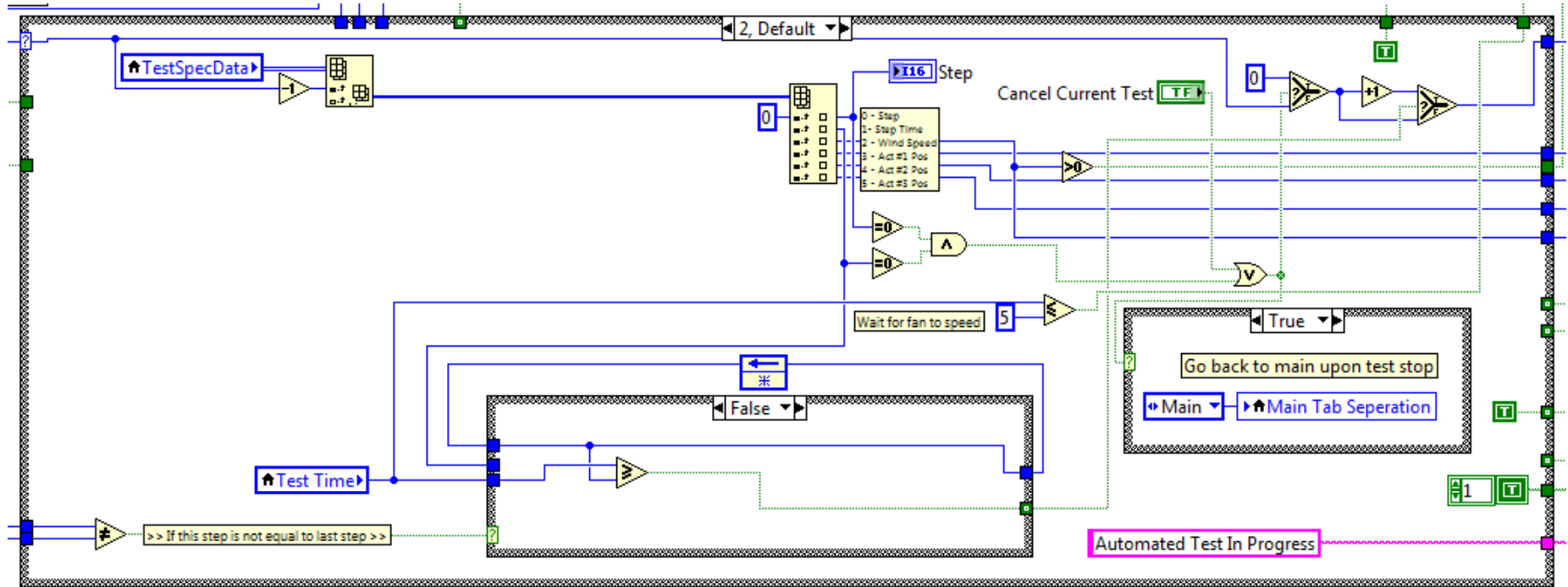
# Automated Test Control



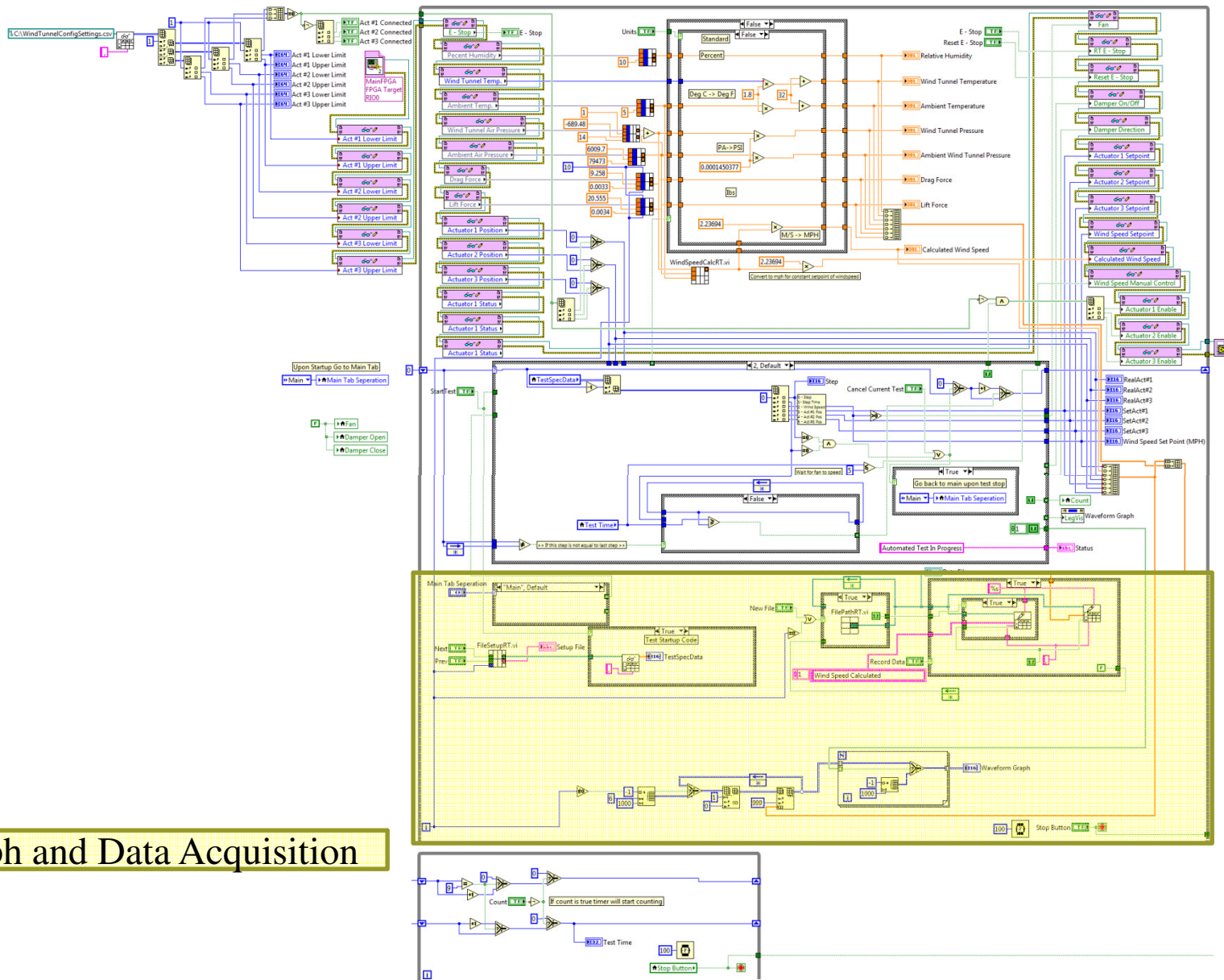
# Automated Test Control



# Automated Test Control

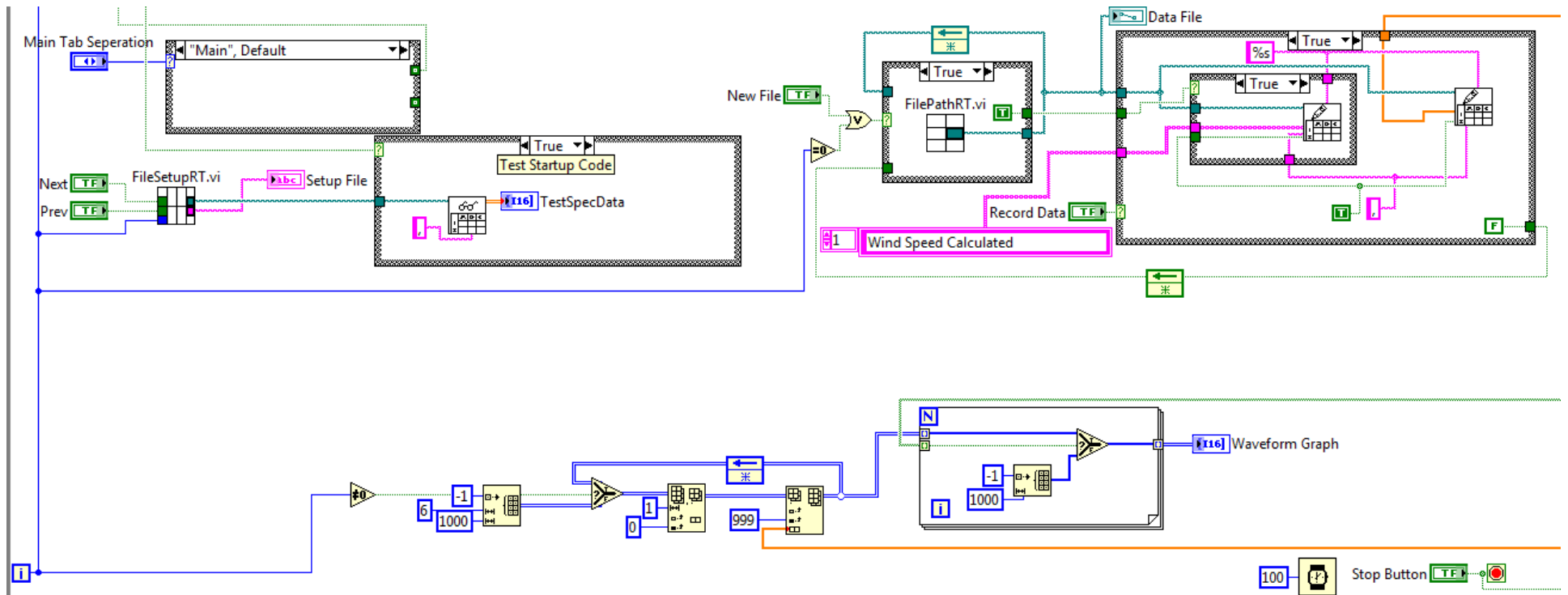


# Real Time Target LabVIEW Code



Graph and Data Acquisition

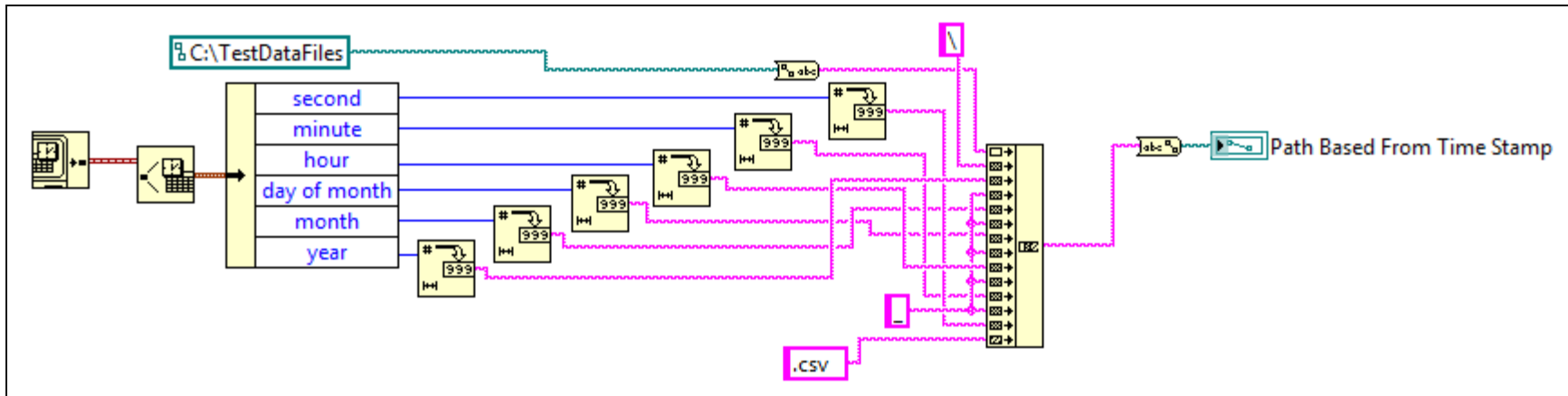
# Graphing and Data Acquisition



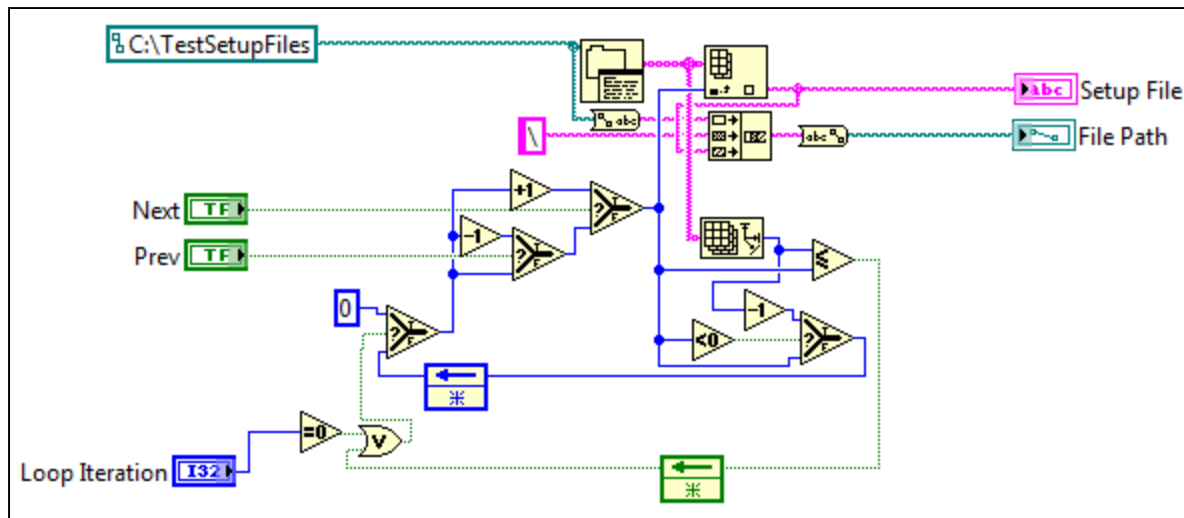
# WindSpeedTest.CSV

Step	Step Time (s)	Wind Speed	#1 Actuator Position (mm)	#2 Actuator Position (mm)	#3 Actuator Position (mm)
0	20	50	120	90	0
1	15	50	90	90	0
2	15	50	150	90	0
3	20	70	120	90	0
4	15	70	90	90	0
5	15	70	150	90	0
6	20	60	120	90	0
7	15	60	90	90	0
8	15	60	150	90	0
9	15	80	120	90	0
10	15	90	120	90	0

# Data Acquisition Sub VIs

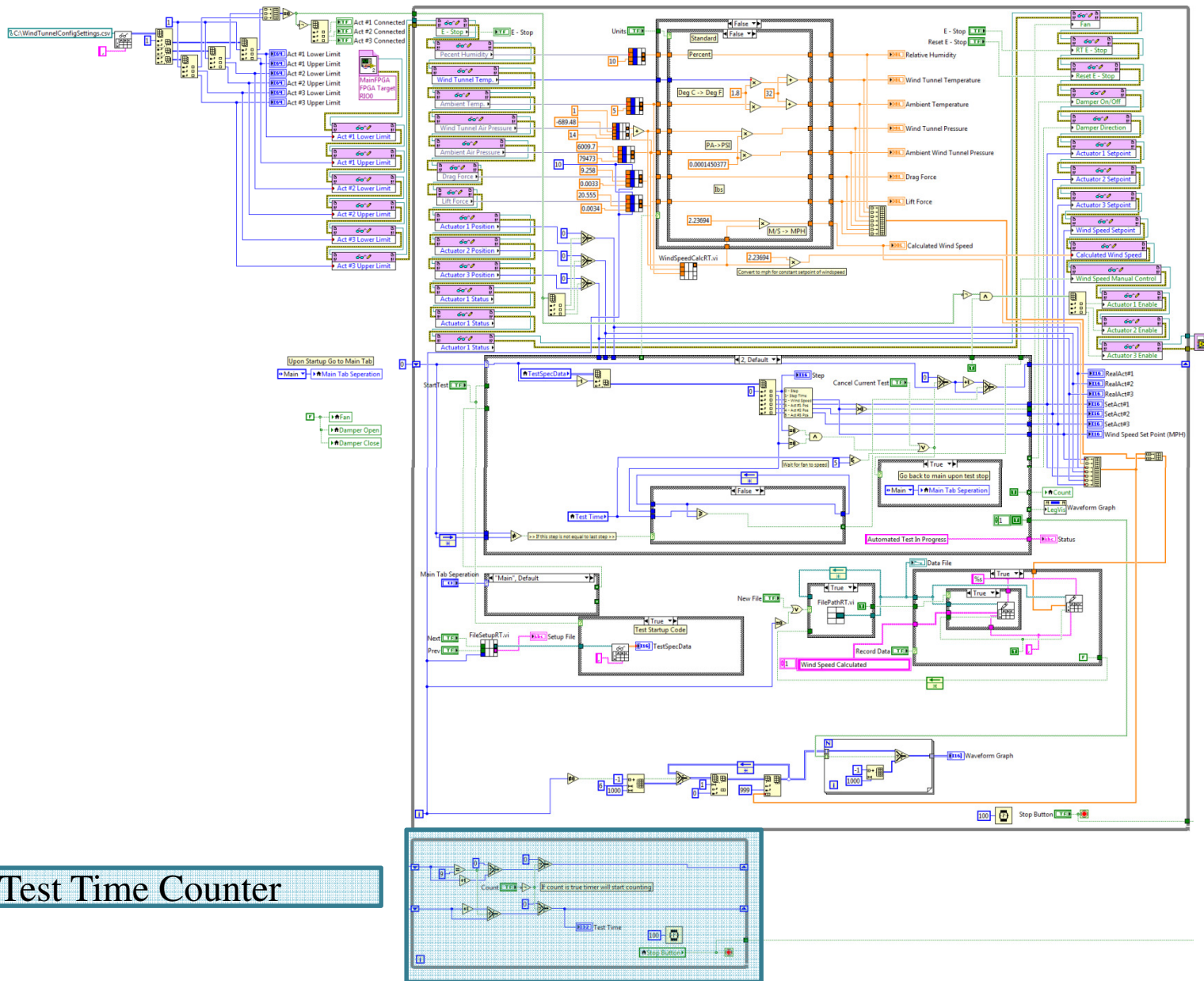


FilePathRT.VI



FileSetupRT.VI

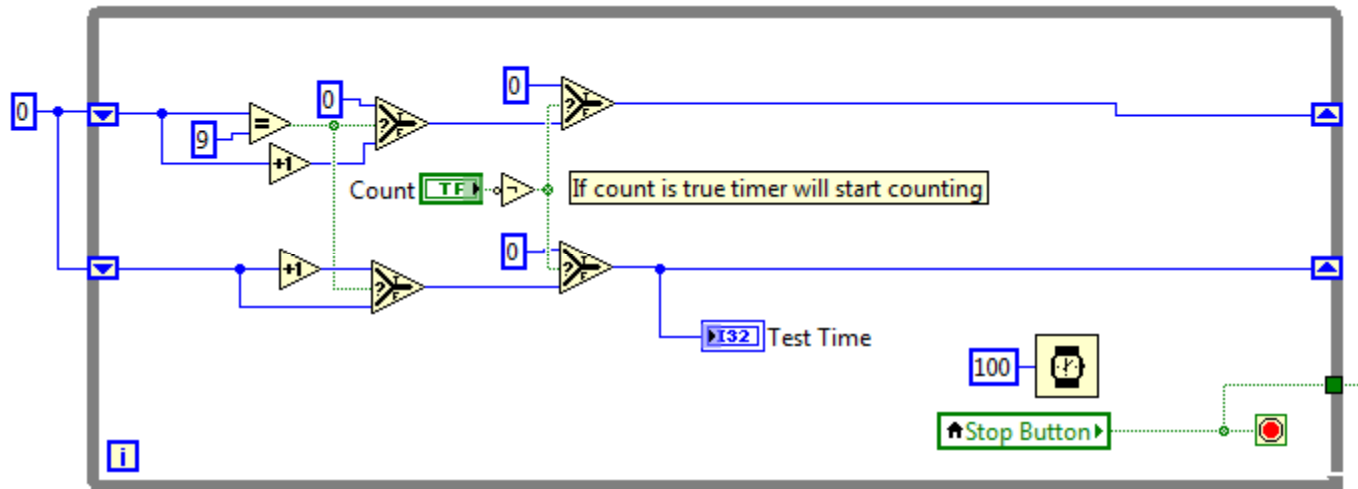
# Real Time Target LabVIEW Code



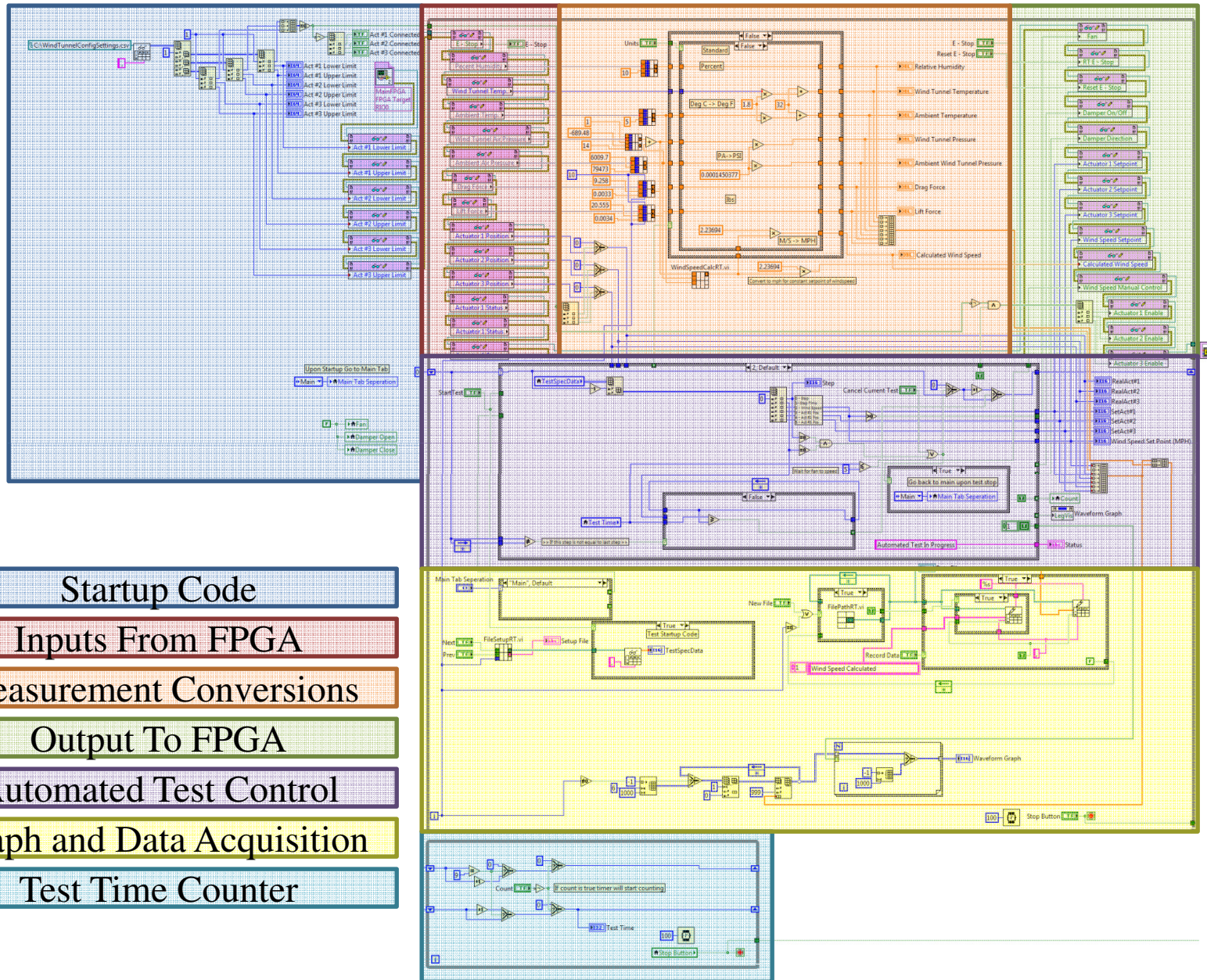
Test Time Counter



# Test Time Counter



# Real Time Target LabVIEW Code



Startup Code

Inputs From FPGA

Measurement Conversions

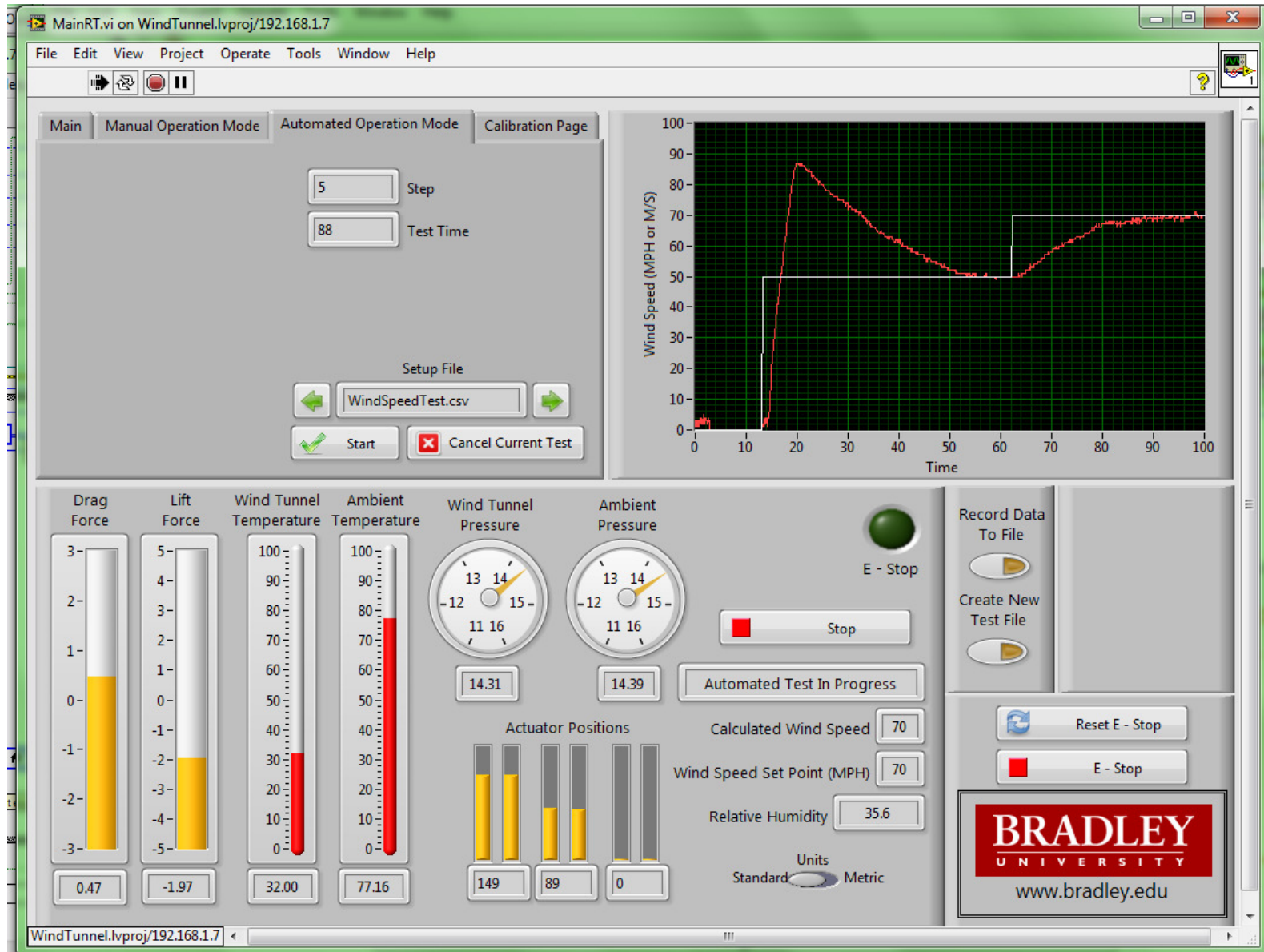
Output To FPGA

Automated Test Control

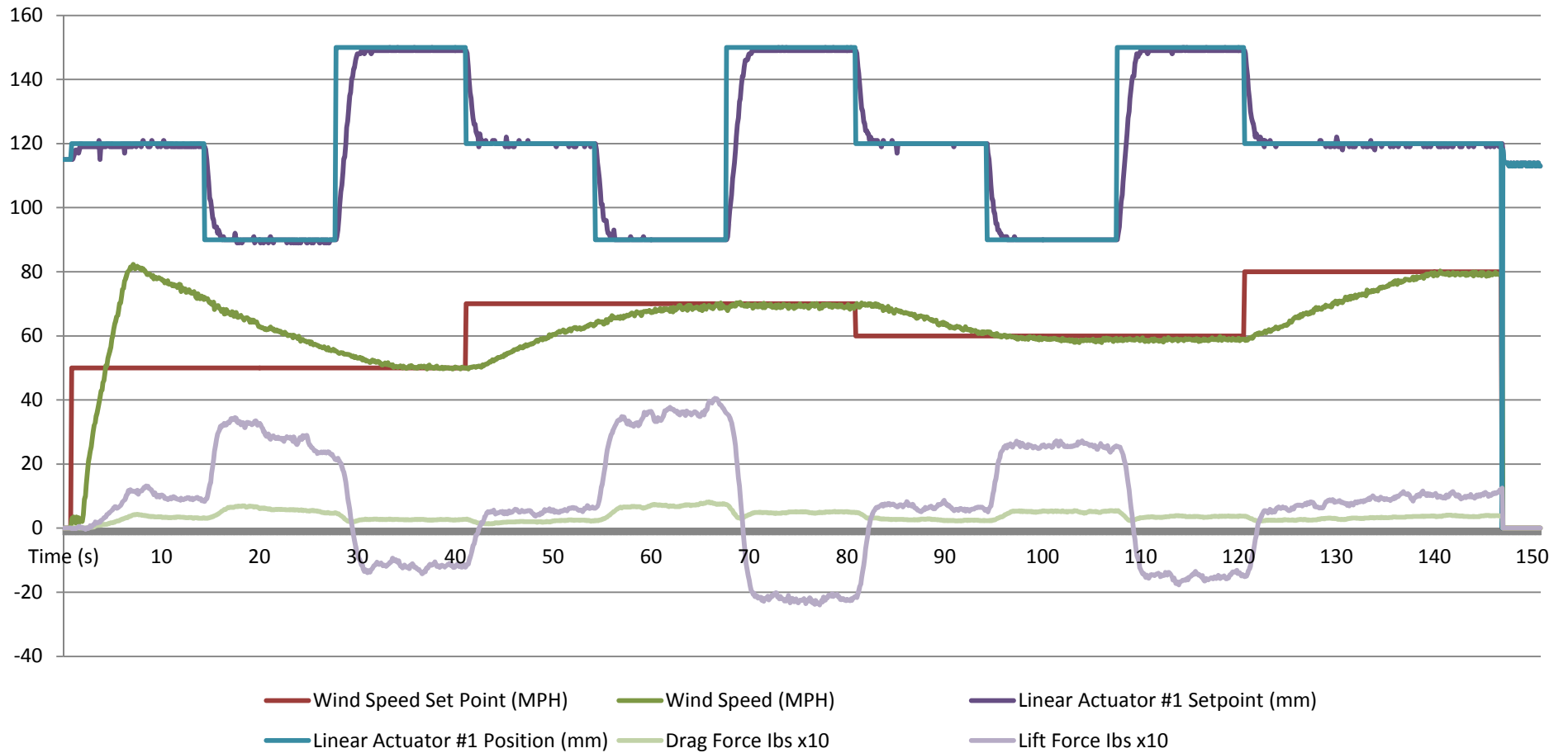
Graph and Data Acquisition

Test Time Counter

# Test Results and Debugging



# Plot From Test Data Recorded to Spreadsheet



# Sources

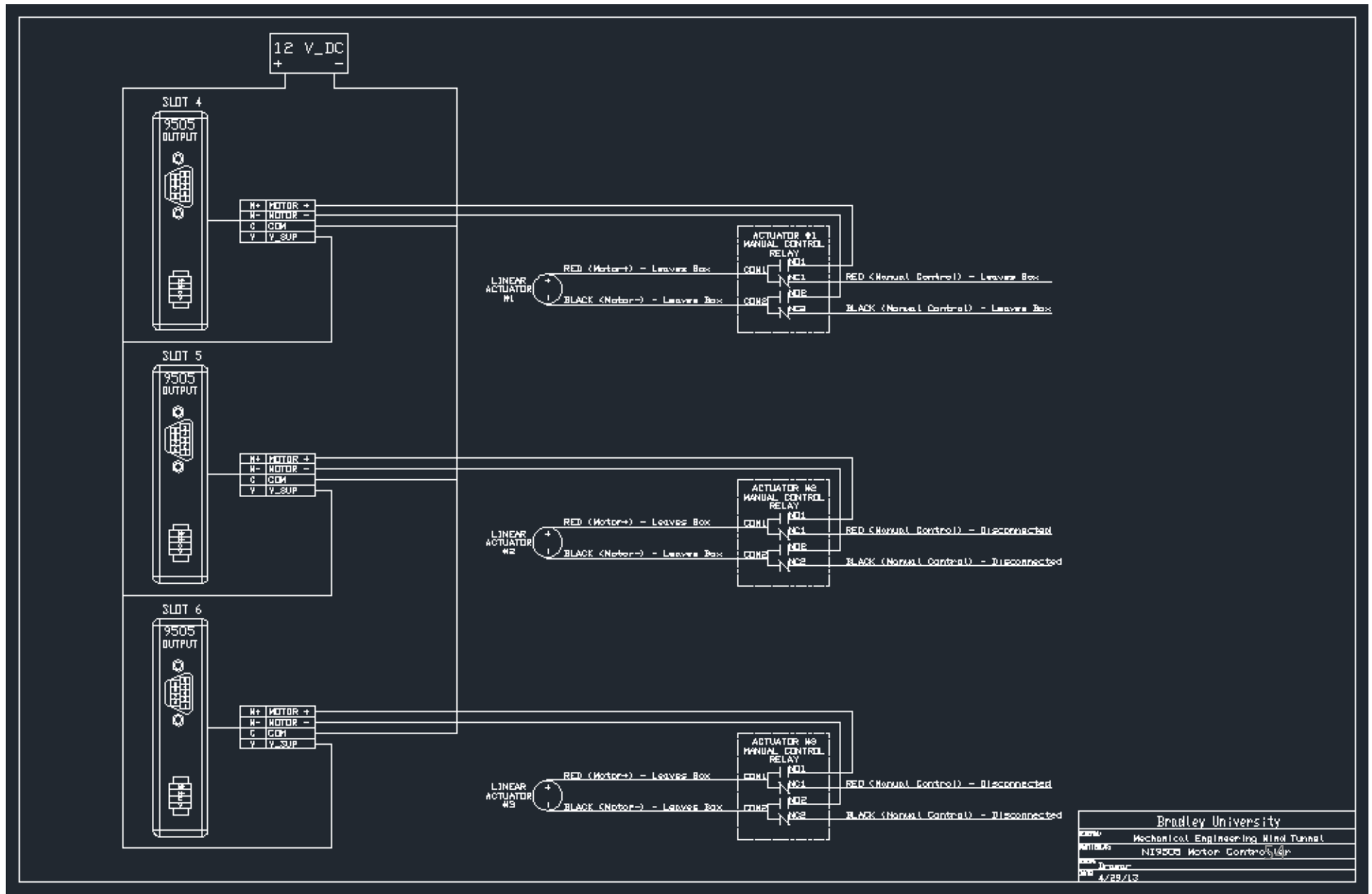
[1] Ben Morrison and Mike Firman. “Web Enabled Wind Tunnel System”, Senior Project, Electrical and Computer Engineering Department, Bradley University, March 2010, <http://cegt201.bradley.edu/projects/proj2010/webwind/>

[2] Nick Detrempe and Daniel Monahan. “Automated Industrial Wind Tunnel Controller”, Senior Project, Electrical and Computer Engineering Department, Bradley University, April 2012, <http://cegt201.bradley.edu/projects/proj2012/aiwt/>

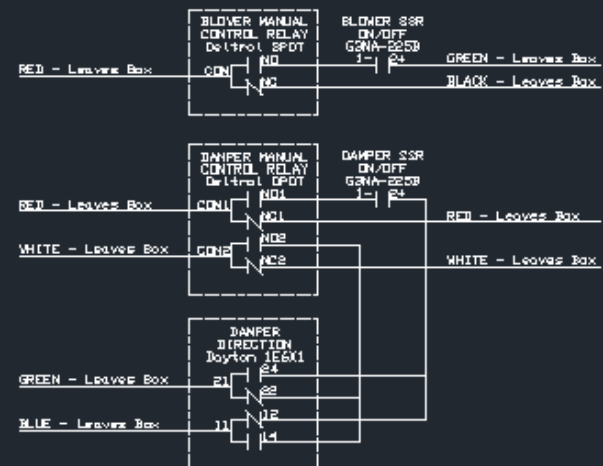
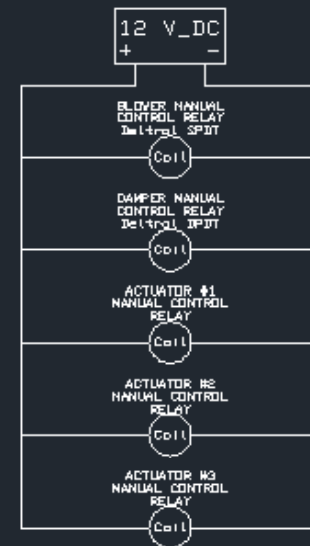
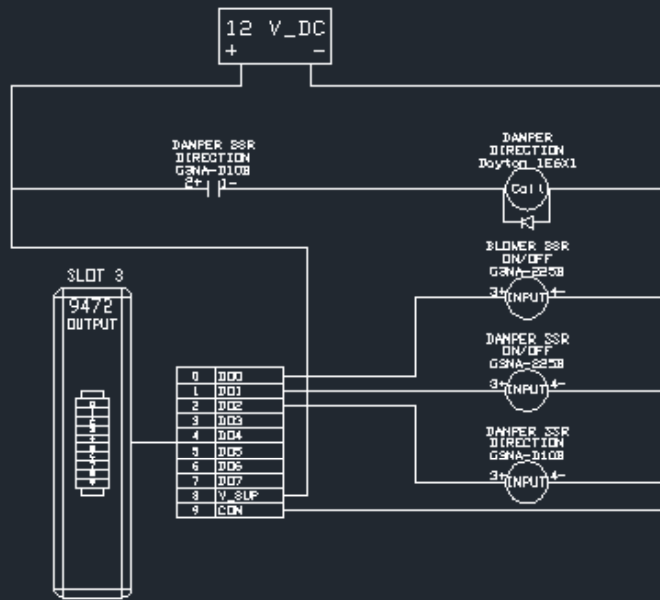
[3] NI CompactRIO, National Instruments, [Online] 2012, <http://www.ni.com/compactrio>



# NI 9505 Wiring Diagram

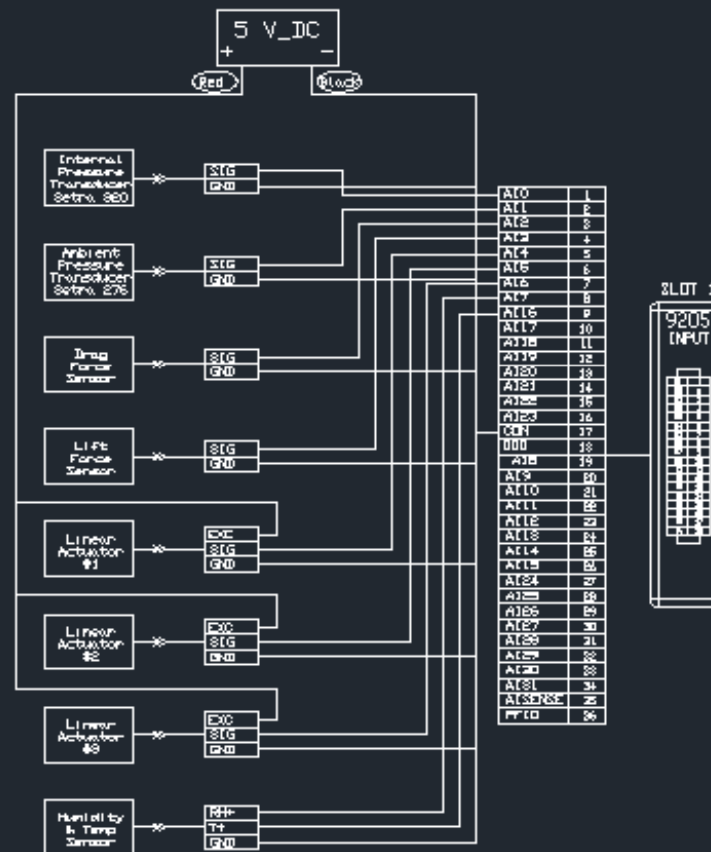


# NI 9472 Wiring Diagram



Bradley University	
REVISION:	Mechanical Engineering Wind Tunnel
NUMBER:	NI9472 Thermocouple Inputs
DATE:	1/20/12
BY:	4/29/12

# NI 9205 Wiring Diagram





# NI 9211 Wiring Diagram

