

**Marie Antoinette Cabucos**  
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## **OBJECTIVE**

To obtain an entry-level position in the field of VHDL and/or VLSI design that would help me utilize my educational experience and gain invaluable work experience.

## **EDUCATION**

Bradley University, Peoria, Illinois.

B.S. Electrical Engineering with computer option, graduates in May 2004.

Richwoods High School, Peoria, Illinois. Graduated May 2000.

Herbert H. Lehman High School, Bronx, New York. Student Freshman to Junior year.

## **SKILLS**

### Engineering:

Knowledge of electric and electronic circuit analysis.

Theoretical knowledge of digital logic design.

Currently studying in courses about Artificial Neural Networks and Semiconductor Physics.

### Computer Languages:

Programming knowledge of C, Microsoft Visual Basic and Pascal.

### Software:

Used Windows 95, Microsoft Word, Excel, and MS Powerpoint extensively in lab and during project presentations.

Built a webpage for the senior project currently in progress.

### Hardware:

Practical knowledge of computer and network assembly and function.

Experienced in the use of L-edit for VLSI design and VHDL coding of FPGAs.

Programmed a home automation system using an EMAC 8051 microcontroller.

## **REFERENCE**

Dr. Vinod B. Prasad, professor

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