

RAHUL CHOPRA

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Objective:

Entry level position in Electrical/Computer Engineering.

Education:

Bradley University, Peoria-IL, Fall 98-Spring 2002.

BS-Electrical/Computer Engineering.

Project Work:

Senior Project: Reliable Data Processor using **VLSI/ASIC**—ALU, Controller, Feedback, Reliability testing, IC Design. <http://cegt201.bradley.edu/projects/proj2002/relidpsr/>

Fall Mini Project: DC Motor Controller using 8051 **Microprocessor**-Assembly Code.

Junior Project: Pulse rate detector using **VHDL** programmed CPLD/FPGA chip.

Course Work:

VLSI/ASIC, DSP, VHDL/FPGA, Computer Architecture, Controls Theory, Programming in C/C++, Microprocessors, Digital Hardware Design, Electronics.

Software Skills:

P-Spice, Mentor Graphics, L-Edit, Xilinx, Verilog, Warp, Matlab, Lattice-Isp, ERP software, MS Visual C++/Basic 6.0, Keil, MS Access2000, Microsoft ODBC.

Academic Honors/Awards: Deans List, Outstanding Student-Fall 2000, 2001.

Work Experience:

GE, Faridabad, India. Summer 2001.

Title: Information Management Trainee

Job Description: **Programming** in MS Visual Basic 6.0, **Green Belt Six Sigma project**, **ERP** (Macola), MS Access Applications, **Database Administration/Tech Support**, Software Development Life Cycles, Technical Documentation.

BHEL, New Delhi, India. Summer 2000

Title: Intern Job Description: **Hardware-Tech Support**, HTML.

Extra-Curricular Activities:

Secretary-Hindu Students Society, Bradley University, Fall 2001. Tennis, Intramural Soccer.